

Course code	Course Name	L-T-P Credits	Year of Introduction
CS301	THEORY OF COMPUTATION	3-1-0-4	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>• To introduce the concept of formal languages.</li> <li>• To discuss the Chomsky classification of formal languages with discussion on grammar and automata for regular, context-free, context sensitive and unrestricted languages.</li> <li>• To discuss the notions of decidability and halting problem.</li> </ul>			
<b>Syllabus</b>			
Introduction to Automata Theory, Structure of an automaton, classification of automata, grammar and automata for generating each class of formal languages in the Chomsky Hierarchy, decidability and Halting problem.			
<b>Expected Outcome</b>			
The Students will be able to			
<ol style="list-style-type: none"> <li>i. Classify formal languages into regular, context-free, context sensitive and unrestricted languages.</li> <li>ii. Design finite state automata, regular grammar, regular expression and Myhill- Nerode relation representations for regular languages.</li> <li>iii. Design push-down automata and context-free grammar representations for context-free languages.</li> <li>iv. Design Turing Machines for accepting recursively enumerable languages.</li> <li>v. Understand the notions of decidability and undecidability of problems, Halting problem.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>1. John E Hopcroft, Rajeev Motwani and Jeffrey D Ullman, Introduction to Automata Theory, Languages, and Computation, 3/e, Pearson Education, 2007</li> <li>2. John C Martin, Introduction to Languages and the Theory of Computation, TMH, 2007</li> <li>3. Michael Sipser, Introduction To Theory of Computation, Cengage Publishers, 2013</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>1. Dexter C. Kozen, Automata and Computability, Springer1999.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
<b>I</b>	Introduction to Automata Theory and its significance. <b>Type 3 Formalism:</b> Finite state automata – Properties of transition functions, Designing finite automata, NFA, Finite Automata with Epsilon Transitions, Equivalence of NFA and DFA, Conversion of NFA to DFA, Equivalence and Conversion of NFA with and without Epsilon Transitions.	<b>10</b>	<b>15 %</b>
<b>II</b>	Myhill-Nerode Theorem, Minimal State FA Computation. Finite State Machines with Output- Mealy and Moore machine (Design Only), Two- Way Finite Automata. Regular Grammar, Regular Expressions, Equivalence of regular expressions and NFA with epsilon transitions. Converting Regular Expressions to NFA with epsilon transitions Equivalence of DFA and regular expressions, converting DFA to Regular Expressions.	<b>10</b>	<b>15 %</b>

<b>FIRST INTERNAL EXAM</b>			
<b>III</b>	Pumping Lemma for Regular Languages, Applications of Pumping Lemma. Closure Properties of Regular sets (Proofs not required), Decision Problems related with Type 3 Formalism <b>Type 2 Formalism:-</b> Context-Free Languages (CFL), Context-Free Grammar (CFG), Derivation trees, Ambiguity, Simplification of CFG, Chomsky Normal Form, Greibach normal forms	<b>09</b>	<b>15 %</b>
<b>IV</b>	Non-Deterministic Pushdown Automata (NPDA), design. Equivalence of acceptance by final state and empty stack in PDA. Equivalence between NPDA and CFG, Deterministic Push Down Automata, Closure properties of CFLs (Proof not required), Decision Problems related with Type 3 Formalism.	<b>08</b>	<b>15 %</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	Pumping Lemma for CFLs, Applications of Pumping Lemma. <b>Type 1 Formalism:</b> Context-sensitive Grammar. Linear Bounded Automata (Design not required) <b>Type 0 Formalism:</b> Turing Machine (TM) – Basics and formal definition, TMs as language acceptors, TMs as Transducers, Designing Turing Machines.	<b>09</b>	<b>20 %</b>
<b>VI</b>	Variants of TMs -Universal Turing Machine, Multi- tape TMs, Non Deterministic TMs, Enumeration Machine (Equivalence not required), Recursively Enumerable Languages, Recursive languages, Properties of Recursively Enumerable Languages and Recursive Languages, Decidability and Halting Problem. Chomsky Hierarchy	<b>08</b>	<b>20 %</b>
<b>END SEMESTER EXAM</b>			

### Question Paper Pattern

1. There will be *five* parts in the question paper – A, B, C, D, E
2. Part A
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules I and II; Allfour questions have to be answered.
3. Part B
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three subparts.
4. Part C
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules III and IV; Allfour questions have to be answered.
5. Part D
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
6. Part E
  - a. Total Marks: 40
  - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered. A question can have a maximum of three sub-parts.

There should be at least 60% analytical/numerical questions.

Course code	Course Name	L-T-P Credits	Year of Introduction
CS303	SYSTEM SOFTWARE	2-1-0-3	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>To make students understand the design concepts of various system software like Assembler, Linker, Loader and Macro pre-processor, Utility Programs such as Text Editor and Debugger.</li> </ul>			
<b>Syllabus</b>			
Different types of System Software, SIC & SIC/XE Architecture and Programming, Basic Functions of Assembler, Assembler Design, Single pass and 2 Pass Assemblers and their Design, Linkers and Loaders, Absolute Loader and Relocating loader, Design of Linking Loader, Macro Processor and its design, Fundamentals of Text Editor Design, Operational Features of Debuggers			
<b>Expected Outcome</b>			
The Students will be able to			
<ol style="list-style-type: none"> <li>distinguish different software into different categories..</li> <li>design, analyze and implement one pass, two pass or multi pass assembler.</li> <li>design, analyze and implement loader and linker.</li> <li>design, analyze and implement macro processors.</li> <li>critique the features of modern editing /debugging tools.</li> </ol>			
<b>Text book</b>			
<ol style="list-style-type: none"> <li>Leland L. Beck, System Software: An Introduction to Systems Programming, 3/E, Pearson Education Asia, 1997.</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>D.M. Dhamdhere, Systems Programming and Operating Systems, Second Revised Edition, Tata McGraw Hill.</li> <li><a href="http://gcc.gnu.org/onlinedocs/gcc-2.95.3/cpp_1.html">http://gcc.gnu.org/onlinedocs/gcc-2.95.3/cpp_1.html</a> - The C Preprocessor</li> <li>J Nithyashri, System Software, Second Edition, Tata McGraw Hill.</li> <li>John J. Donovan, Systems Programming, Tata McGraw Hill Edition 1991.</li> <li>Jonathan Corbet, Alessandro Rubini, Greg Kroah-Hartman, Linux Device Drivers, Third Edition, O.Reilly Books</li> <li>M. Beck, H. Bohme, M. Dziadzka, et al., Linux Kernel Internals, Second Edition, Addison Wesley Publications,</li> <li>Peter Abel, IBM PC Assembly Language and Programming, Third Edition, Prentice Hall of India.</li> <li>Writing UNIX device drivers - George Pajari – Addison Wesley Publications (Ebook : <a href="http://tocs.ulb.tu-darmstadt.de/197262074.pdf">http://tocs.ulb.tu-darmstadt.de/197262074.pdf</a> ).</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem Exam. Marks

<b>I</b>	<b>Introduction :</b> System Software Vs. Application Software, Different System Software– Assembler, Linker, Loader, Macro Processor, Text Editor,	<b>2</b>	<b>15%</b>
	Debugger, Device Driver, Compiler, Interpreter, Operating System(Basic Concepts only) SIC & SIC/XE Architecture, Addressing modes, SIC & SIC/XE Instruction set, Assembler Directives and Programming.	<b>6</b>	
<b>II</b>	<b>Assemblers</b> Basic Functions of Assembler. Assembler output format – Header, Text and End Records- Assembler data structures, Two pass assembler algorithm, Hand assembly of SIC/XE program, Machine dependent assembler features.	<b>6</b>	<b>15 %</b>
<b>FIRST INTERNAL EXAM</b>			
<b>III</b>	<b>Assembler design options:</b> Machine Independent assembler features – program blocks, Control sections, Assembler design options- Algorithm for Single Pass assembler, Multi pass assembler, Implementation example of MASM Assembler	<b>7</b>	<b>15 %</b>
<b>IV</b>	<b>Linker and Loader</b> Basic Loader functions - Design of absolute loader, Simple bootstrap Loader, Machine dependent loader features- Relocation, Program Linking, Algorithm and data structures of two pass Linking Loader, Machine dependent loader features, Loader Design Options.	<b>7</b>	<b>15 %</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	<b>Macro Preprocessor:-</b> Macro Instruction Definition and Expansion. One pass Macro processor Algorithm and data structures, Machine Independent Macro Processor Features, Macro processor design options	<b>7</b>	<b>20 %</b>
<b>VI</b>	<b>Device drivers:</b> Anatomy of a device driver, Character and block device drivers, General design of device drivers	<b>2</b>	<b>20 %</b>
	<b>Text Editors:</b> Overview of Editing, User Interface, Editor Structure.	<b>2</b>	
	<b>Debuggers :-</b> Debugging Functions and Capabilities, Relationship with other parts of the system, Debugging Methods- By Induction, Deduction and Backtracking.	<b>4</b>	
<b>END SEMESTER EXAM</b>			

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  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules I and II; Allfour questions have to be answered.
3. Part B
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three subparts.
4. Part C
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules III and IV; Allfour questions have to be answered.
5. Part D
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
6. Part E
  - a. Total Marks: 40
  - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
  - c. A question can have a maximum of three sub-parts.
7. There should be at least 60% analytical/numerical questions.



Course code	Course Name	L-T-P - Credits	Year of Introduction
CS305	Microprocessors and Microcontrollers	2-1-0-3	2016
<b>Prerequisite: CS202 Computer Organisation and Architecture</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>To impart basic understanding of the internal organisation of 8086 Microprocessor and 8051 microcontroller.</li> <li>To introduce the concepts of interfacing microprocessors with external devices.</li> <li>To develop Assembly language programming skills.</li> </ul>			
<b>Syllabus</b>			
Introduction to 8086 Microprocessor; Architecture and signals, Instruction set of 8086, Timing Diagram, Assembly Language Programming, Memory and I/O interfacing, Interfacing with 8255, 8279, 8257, Interrupts and Interrupt handling, Microcontrollers - 8051 Architecture and its salient features, Instruction Set and Simple Programming Concepts.			
<b>Expected Outcome</b>			
The Students will be able to			
<ol style="list-style-type: none"> <li>Describe different modes of operations of a typical microprocessor and microcontroller.</li> <li>Design and develop 8086 assembly language programs using software interrupts and various assembler directives.</li> <li>Interface microprocessors with various external devices.</li> <li>Analyze and compare the features of microprocessors and microcontrollers.</li> <li>Design and develop assembly language programs using 8051 microcontroller.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>Bhurchandi and Ray, <i>Advanced Microprocessors and Peripherals</i>, Third Edition McGraw Hill, 2012</li> <li>Raj Kamal, <i>Microcontrollers: Architecture, Programming, Interfacing and System Design</i>, Pearson Education, 2011.</li> <li>Douglas V. Hall, SSSP Rao, <i>Microprocessors and Interfacing</i>, Third Edition, McGrawHill Education, 2012.</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>Barry B. Brey, <i>The Intel Microprocessors – Architecture, Programming and Interfacing</i>, Eighth Edition, Pearson Education, 2015</li> <li>A. NagoorKani, <i>Microprocessors and Microcontrollers</i>, Second Edition, Tata McGraw Hill, 2012.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
I	Evolution of microprocessors, 8086 Microprocessor - Architecture and signals, Memory organisation, Minimum and maximum mode of operation, Minimum mode Timing Diagram. Comparison of 8086 and 8088.	07	15%
II	8086 Addressing Modes, 8086 Instruction set and Assembler Directives - Assembly Language Programming with Subroutines, Macros, Passing Parameters, Use of stack.	08	15%

<b>FIRST INTERNAL EXAM</b>			
<b>III</b>	Interrupts - Types of Interrupts and Interrupt Service Routine. Handling Interrupts in 8086, Interrupt programming. Basic Peripherals and their Interfacing with 8086 - Programmable Interrupt Controller - 8259 - Architecture.	<b>07</b>	<b>15%</b>
<b>IV</b>	Interfacing Memory, I/O, 8255 - Detailed study - Architecture, Control word format and modes of operation, Architecture and modes of operation of 8279 and 8257 (Just mention the control word, no need to memorize the control word format)	<b>07</b>	<b>15%</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	Microcontrollers - Types of Microcontrollers - Criteria for selecting a microcontroller - Example Applications. Characteristics and Resources of a microcontroller. Organization and design of these resources in a typical microcontroller - 8051. 8051 Architecture, Register Organization, Memory and I/O addressing, Interrupts and Stack.	<b>08</b>	<b>20%</b>
<b>VI</b>	8051 Addressing Modes, Different types of instructions and Instruction Set, Simple programs. Peripheral Chips for timing control - 8254/8253.	<b>08</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

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- Part B
  - Total marks : 18
  - Three* questions each having 2 marks, uniformly covering modules I and II; *Two* questions have to be answered. Each question can have a maximum of three subparts.
- Part C
  - Total marks : 12
  - Four* questions each having 3 marks, uniformly covering modules III and IV; *Allfour* questions have to be answered.
- Part D
  - Total marks : 18
  - Three* question each having 2 marks, uniformly covering modules III and IV; *Two* questions have to be answered. Each question can have a maximum of three subparts
- Part E
  - Total Marks: 40
  - Six* questions each carrying 10 marks, uniformly covering modules V and VI; *four* questions have to be answered.
  - A question can have a maximum of three sub-parts.
- There should be at least 60% analytical/numerical questions.

Course code.	Course Name	L-T-P-Credits	Year of Introduction
CS307	DATA COMMUNICATION	3-0-0-3	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To introduce fundamental communication models.</li> <li>• To discuss various time domain and frequency domain concepts of data communication.</li> <li>• To introduce the concepts of encoding, multiplexing and spread spectrum.</li> </ul>			
<b>Syllabus</b> Data Transmission, Transmission Impairments, Channel Capacity, Transmission media, Wireless propagation, Signal encoding Techniques, Multiplexing, Digital data transmission techniques, Sampling theorem, Error detection and correction, Spread spectrum, Basic principles of switching.			
<b>Expected Outcome</b> The Students will be able to <ol style="list-style-type: none"> <li>i. Identify and list the various issues present in the design of a data communication system.</li> <li>ii. Apply the time domain and frequency domain concepts of signals in data communication.</li> <li>iii. Compare and select transmission media based on transmission impairments and channel capacity.</li> <li>iv. Select and use appropriate signal encoding techniques and multiplexing techniques for a given scenario.</li> <li>v. Design suitable error detection and error correction algorithms to achieve error free data communication and explain different switching techniques.</li> </ol>			
<b>Text Books</b> <ol style="list-style-type: none"> <li>1. Curt M. White, Fundamentals of Networking and Communication 7/e, Cengage learning. [Chapter 3,4,9,10]</li> <li>2. Forouzan B. A., Data Communications and Networking, 5/e, Tata McGraw Hill, 2013. [Chapters:3,4, 5, 6,7,8]</li> <li>3. Schiller J., Mobile Communications, 2/e, Pearson Education, 2009. [Chapters:2,3]</li> <li>4. William Stallings, Data and Computer Communication 9/e, Pearson Education, Inc. [Chapters: 4, 5, 6, 7, 8, 9].</li> </ol>			
<b>References</b> <ol style="list-style-type: none"> <li>1. Forouzan B. A., Data Communications and Networking, 4/e, Tata McGraw Hill, 2007.</li> <li>2. Tanenbaum A. S. and D. Wetherall, Computer Networks, Pearson Education, 2013.</li> </ol>			
<b>COURSE PLAN</b>			
Module	Contents	Hours	End Sem. Exam Marks

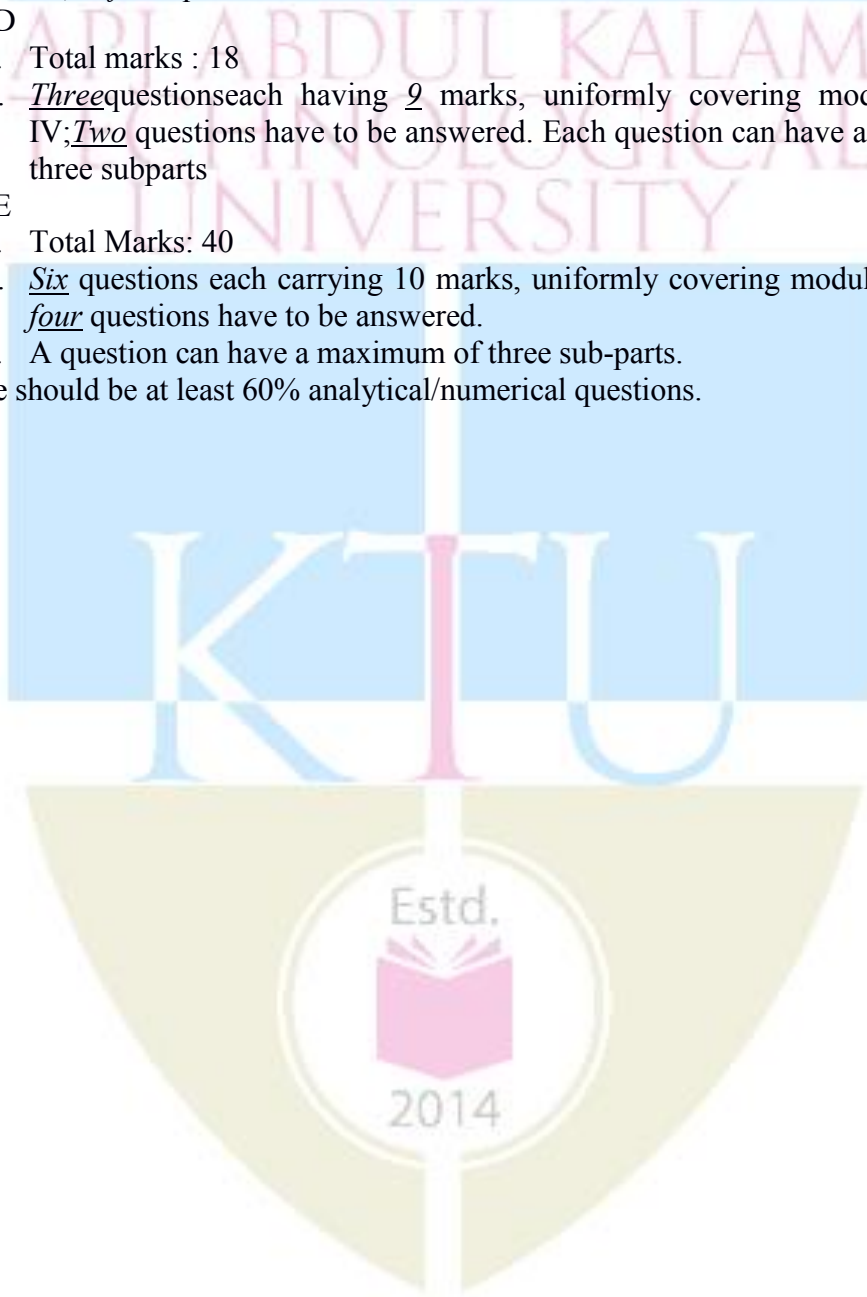


<b>I</b>	Data Transmission: Communication model Simplex, half duplex and full duplex transmission - Periodic Analog signals: Sine wave, phase, wavelength, time and frequency domain, bandwidth - Digital Signals; Digital data Transmission:- Analog & Digital data, Analog & Digital signals, Analog & Digital transmission – Transmission Impairments: Attenuation, Delay distortion, Noise - Channel capacity: Nyquist Bandwidth, Shannon's Capacity formula.	<b>08</b>	<b>15%</b>
<b>II</b>	Transmission media - Guided Transmission Media: Twisted pair, Coaxial cable, optical fiber, Wireless Transmission, Terrestrial microwave, Satellite microwave. Wireless Propagation: Ground wave propagation, Sky Wave propagation, LoS Propagation.	<b>07</b>	<b>15%</b>
<b>FIRST INTERNAL EXAM</b>			
<b>III</b>	Signal Encoding techniques - Digital Data Digital Signals: NRZ, Multilevel binary, Biphase - Digital Data Analog Signals : ASK, FSK, PSK - Analog Data Digital Signals: Sampling theorem, PCM, Delta Modulation - Analog Data Analog Signals: AM, FM, PM.	<b>07</b>	<b>15%</b>
<b>IV</b>	Multiplexing- Space Division Multiplexing-Frequency Division Multiplexing: Wave length Division Multiplexing - Time Division multiplexing: Characteristics, Digital Carrier system, SONET/SDH-Statistical time division multiplexing: Cable Modem - Code Division Multiplexing. Multiple Access– CDMA.	<b>07</b>	<b>15%</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	Digital Data Communication Techniques - Asynchronous transmission, Synchronous transmission-Detecting and Correcting Errors-Types of Errors-Error Detection: Parity check, Cyclic Redundancy Check (CRC) - Error Control Error Correction: Forward Error Correction and Hamming Distance.	<b>06</b>	<b>20%</b>
<b>VI</b>	Spread Spectrum Techniques-Direct Sequence Spread Spectrum (DSSS), Frequency Hopping Spread Spectrum (FHSS). Basic principles of switching - Circuit Switched Networks, Structure of Circuit Switch - Packet Switching: Datagram Networks, Virtual Circuit Networks, Structure of packet switches.	<b>07</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

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3. Part B

- a. Total marks : 18
  - b. Three questions each having 2 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three subparts.
4. Part C
- a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules III and IV; All four questions have to be answered.
5. Part D
- a. Total marks : 18
  - b. Three questions each having 2 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
6. Part E
- a. Total Marks: 40
  - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
  - c. A question can have a maximum of three sub-parts.
7. There should be at least 60% analytical/numerical questions.



Course code	Course Name	L-T-P Credits	Year of Introduction
CS309	GRAPH THEORY AND COMBINATORICS	2-0-2-3	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>To introduce the fundamental concepts in graph theory, including properties and characterization of graphs/ trees and Graphs theoretic algorithms</li> </ul>			
<b>Syllabus</b>			
Introductory concepts of graphs, Euler and Hamiltonian graphs, Planar Graphs, Trees, Vertex connectivity and edge connectivity, Cut set and Cut vertices, Matrix representation of graphs, Graphs theoretic algorithms.			
<b>Expected Outcome</b>			
The Students will be able to			
<ol style="list-style-type: none"> <li>Demonstrate the knowledge of fundamental concepts in graph theory, including properties and characterization of graphs and trees.</li> <li>Use graphs for solving real life problems.</li> <li>Distinguish between planar and non-planar graphs and solve problems.</li> <li>Develop efficient algorithms for graph related problems in different domains of engineering and science.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>Douglas B. West, Introduction to Graph Theory, Prentice Hall India Ltd., 2001</li> <li>Narasimha Deo, Graph theory, PHI, 1979.</li> <li>Robin J. Wilson, Introduction to Graph Theory, Longman Group Ltd., 2010</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>R. Diestel, <i>Graph Theory</i>, free online edition, 2016: <a href="http://diestel-graph-theory.com/basic.html">diestel-graph-theory.com/basic.html</a>.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
I	<b>Introductory concepts</b> - What is graph – Application of graphs – finite and infinite graphs – Incidence and Degree – Isolated vertex, pendent vertex and Null graph. Paths and circuits – Isomorphism, sub graphs, walks, paths and circuits, Connected graphs, disconnect graphs.	09	15 %
II	Euler graphs, Hamiltonian paths and circuits, Dirac's theorem for Hamiltonicity, Travelling salesman problem. Directed graphs – types of digraphs, Digraphs and binary relation	10	15 %
<b>FIRST INTERNAL EXAM</b>			
III	Trees – properties, pendent vertex, Distance and centres - Rooted and binary tree, counting trees, spanning trees.	07	15 %
IV	Vertex Connectivity, Edge Connectivity, Cut set and Cut Vertices, Fundamental circuits, Planar graphs, Different representation of planar graphs, Euler's theorem, Geometric dual, Combinatorial dual.	09	15 %
<b>SECOND INTERNAL EXAM</b>			

V	Matrix representation of graphs- Adjacency matrix, Incidence Matrix, Circuit matrix, Fundamental Circuit matrix and Rank, Cut set matrix, Path matrix	08	20 %
VI	Graphs theoretic algorithms - Algorithm for computer representation of a graph, algorithm for connectedness and components, spanning tree, shortest path.	07	20 %
<b>END SEMESTER EXAM</b>			

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5. Part D
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6. Part E
  - a. Total Marks: 40
  - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
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7. There should be at least 60% analytical/numerical questions.

Course code	Course Name	L-T-P Credits	Year of Introduction
CS361	SOFT COMPUTING	3-0-0-3	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>To introduce the concepts in Soft Computing such as Artificial Neural Networks, Fuzzy logic-based systems, genetic algorithm-based systems and their hybrids.</li> </ul>			
<b>Syllabus</b>			
Introduction to Soft Computing, Artificial Neural Networks, Fuzzy Logic and Fuzzy systems, Genetic Algorithms, hybrid systems.			
<b>Expected Outcome</b>			
The Students will be able to			
<ol style="list-style-type: none"> <li>Learn soft computing techniques and their applications.</li> <li>Analyze various neural network architectures.</li> <li>Define the fuzzy systems.</li> <li>Understand the genetic algorithm concepts and their applications.</li> <li>Identify and select a suitable Soft Computing technology to solve the problem; construct a solution and implement a Soft Computing solution.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>S. N. Sivanandam and S. N. Deepa, Principles of soft computing – John Wiley &amp; Sons, 2007.</li> <li>Timothy J. Ross, Fuzzy Logic with engineering applications, John Wiley &amp; Sons, 2016.</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>N. K. Sinha and M. M. Gupta, Soft Computing &amp; Intelligent Systems: Theory &amp; Applications-Academic Press /Elsevier. 2009.</li> <li>Simon Haykin, Neural Network- A Comprehensive Foundation- Prentice Hall International, Inc.1998</li> <li>R. Eberhart and Y. Shi, Computational Intelligence: Concepts to Implementation, Morgan Kaufman/Elsevier, 2007.</li> <li>Driankov D., Hellendoorn H. and Reinfrank M., An Introduction to Fuzzy Control- Narosa Pub., 2001.</li> <li>Bart Kosko, Neural Network and Fuzzy Systems- Prentice Hall, Inc., Englewood Cliffs, 1992</li> <li>Goldberg D.E., Genetic Algorithms in Search, Optimization, and Machine Learning- Addison Wesley, 1989.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
<b>I</b>	Introduction to Soft Computing Artificial neural networks - biological neurons, Basic models of artificial neural networks – Connections, Learning, Activation Functions, McCulloch and Pitts Neuron, Hebb network.	<b>07</b>	<b>15%</b>
<b>II</b>	Perceptron networks – Learning rule – Training and testing algorithm, Adaptive Linear Neuron, Back propagation Network – Architecture, Training algorithm	<b>07</b>	<b>15%</b>
<b>FIRST INTERNAL EXAM</b>			

<b>III</b>	Fuzzy logic - fuzzy sets - properties - operations on fuzzy sets, fuzzy relations - operations on fuzzy relations	<b>07</b>	<b>15%</b>
<b>IV</b>	Fuzzy membership functions, fuzzification, Methods of membership value assignments – intuition – inference – rank ordering, Lambda – cuts for fuzzy sets, Defuzzification methods	<b>07</b>	<b>15%</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	Truth values and Tables in Fuzzy Logic, Fuzzy propositions, Formation of fuzzy rules - Decomposition of rules – Aggregation of rules, Fuzzy Inference Systems - Mamdani and Sugeno types, Neuro-fuzzy hybrid systems – characteristics - classification	<b>07</b>	<b>20%</b>
<b>VI</b>	Introduction to genetic algorithm, operators in genetic algorithm - coding - selection - cross over – mutation, Stopping condition for genetic algorithm flow, Genetic-neuro hybrid systems, Genetic-Fuzzy rule based system	<b>07</b>	<b>20%</b>
<b>END SEMESTER EXAMINATION</b>			

### Question Paper Pattern

1. There will be *five* parts in the question paper – A, B, C, D, E
2. Part A
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules I and II; All four questions have to be answered.
3. Part B
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three sub-parts
4. Part C
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules III and IV; All four questions have to be answered.
5. Part D
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
6. Part E
  - a. Total Marks: 40
  - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
  - c. A question can have a maximum of three sub-parts.
7. There should be at least 60% analytical/numerical/design questions.

Course code	Course Name	L-T-P Credits	Year of Introduction
CS363	Signals and Systems	3-0-0-3	2016
<b>Pre-requisite: NIL</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>• To introduce fundamental concepts of continuous time and discrete time signals.</li> <li>• To introduce fundamental concepts of continuous time and discrete time systems.</li> <li>• To introduce frequency domain representation and analysis of signals.</li> </ul>			
<b>Syllabus</b>			
Signals and systems –basic operations on signals – continuous time and discrete time signals – Continuous time and discrete time systems –properties of systems - Z-transform – region of convergence – properties of Z-transform – inverse Z-transform. Fourier transform (FT) of discrete time signals – properties of FT – relation between Z-transform and FT. Discrete Fourier transform (DFT) - Properties of DFT – inverse DFT - Fast Fourier transform (FFT) - Radix-2 FFT algorithms – butterfly structure. Digital filter structures –structures for IIR - Structures for FIR.			
<b>Expected Outcome</b>			
The Students will be able to <ol style="list-style-type: none"> <li>i. Identify different types of continuous time and discrete time signals.</li> <li>ii. Identify different types of continuous time and discrete time systems.</li> <li>iii. Analyse signals using Z Transform and FT.</li> <li>iv. Analyse signals using DFT and FFT.</li> <li>v. Appreciate IIR digital filter structures.</li> <li>vi. Appreciate FIR digital filter structures.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>1. M.N. Bandyopadhyaya , Introduction to Signals and Systems and Digital Signal Processing, PHI, 2005.</li> <li>2. S.D. Apte, Digital Signal Processing , Wiley India, 2012.</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>1. A. Ambardar, Digital Signal Processing: A Modern Introduction, Thomson India Edition, 2007.</li> <li>2. A.V. Oppenheim and R. W. Schaffer, Discrete Time Signal Processing (Prentice Hall Signal Processing Series), 3e, Pearson, 2009.</li> <li>3. D. Ganesh Rao and V. P. Gejji, Digital Signal Processing Theory and Lab Practice, Pearson Education Ltd.</li> <li>4. J.K. Proakis and D.G. Manolakis, Introduction to Digital Signal Processing, MacMillan, 1989</li> <li>5. Li Tan , Digital Signal Processing, Fundamentals and Applications, Elsevier, 2013.</li> <li>6. M. H. Hayes, Digital Signal Processing, McGraw Hill (SCHAUM’S Outlines), 2011.</li> <li>7. P. Ramesh Babu, Digital Signal Processing, Scitech Publications, 2012.</li> <li>8. S.K. Mitra, Digital Signal Processing, McGraw Hill Education, 2013.</li> <li>9. S.W. Smith, Digital Signal Processing : A Practical Guide for Engineers and Scientists, Elsevier India.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks

<b>I</b>	Signals and systems – introduction – basic operations on signals – continuous time and discrete time signals –step, impulse, ramp, exponential and sinusoidal functions.	<b>07</b>	<b>15 %</b>
<b>II</b>	Continuous time and discrete time systems –properties of systems – linearity, causality, time invariance, memory, stability, invertibility. Linear time invariant systems – convolution.	<b>07</b>	<b>15 %</b>
<b>FIRST INTERNAL EXAM</b>			
<b>III</b>	Z-transform – region of convergence – properties of Z-transform – inverse Z-transform. Fourier transform (FT) of discrete time signals – properties of FT – relation between Z-transform and FT.	<b>07</b>	<b>15 %</b>
<b>IV</b>	Discrete Fourier transform (DFT) - Properties of DFT – inverse DFT - Fast Fourier transform (FFT) - Radix-2 FFT algorithms – butterfly structure.	<b>07</b>	<b>15 %</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	Digital filter structures – block diagram and signal flow graph representation – structures for IIR – direct form structure – Cascade form structure – parallel form structure – lattice structure.	<b>07</b>	<b>20 %</b>
<b>VI</b>	Structures for FIR – direct form structures – direct form structure of linear phase system – cascade form structure – frequency sampling structure – lattice structure.	<b>07</b>	<b>20 %</b>
<b>END SEMESTER EXAM</b>			

#### Question Paper Pattern

1. There will be *five* parts in the question paper – A, B, C, D, E
2. Part A
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules I and II; All four questions have to be answered.
3. Part B
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three subparts
4. Part C
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules III and IV; All four questions have to be answered.
5. Part D
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
6. Part E
  - a. Total Marks: 40
  - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
  - c. A question can have a maximum of three sub-parts.

There should be at least 60% analytical/numerical questions



Course code	Course Name	L-T-P-Credits	Year of Introduction
CS365	OPTIMIZATION TECHNIQUES	3-0-0-3	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>• To build an understanding on the basics of optimization techniques.</li> <li>• To introduce basics of linear programming and meta- heuristic search techniques.</li> </ul>			
<b>Syllabus</b>			
Basics of Operations Research - Formulation of optimization problems - Linear Programming - Transportation Problem - Assignment Problem - Network flow Problem - Tabu Search - Genetic Algorithm - Simulated Annealing – Applications.			
<b>Expected Outcome</b>			
The Students will be able to			
<ol style="list-style-type: none"> <li>i. Formulate mathematical models for optimization problems.</li> <li>ii. Analyze the complexity of solutions to an optimization problem.</li> <li>iii. Design programs using meta-heuristic search concepts to solve optimization problems.</li> <li>iv. Develop hybrid models to solve an optimization problem.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>1. G. Zapfel, R. Barune and M. Bogl, Meta heuristic search concepts: A tutorial with applications to production and logistics, Springer, 2010.</li> <li>2. Hamdy A. Taha, Operations Research – An introduction, Pearson Education, 2010.</li> <li>3. Rao S.S., Optimization Theory and Applications, Wiley Eastern, 1984.</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>1. Gass S. I., Introduction to Linear Programming, Tata McGraw Hill.</li> <li>2. Goldberg, Genetic algorithms in Search, optimization and Machine Learning, Addison Wesley, 1989.</li> <li>3. K. Deb, Optimization for engineering design – algorithms and examples, Prentice Hall of India, 2004.</li> <li>4. Reeves C., Modern heuristic techniques for combinatorial problems, Orient Longman, 1993.</li> </ol>			
<b>COURSE PLAN</b>			
Module	Contents	Hours	End Sem. Exam Marks
<b>I</b>	Decision-making procedure under certainty and under uncertainty - Operations Research-Probability and decision- making- Queuing or Waiting line theory-Simulation and Monte- Carlo Technique- Nature and organization of optimization problems- Scope and hierarchy of optimization- Typical applications of optimization.	<b>08</b>	<b>15%</b>
<b>II</b>	Essential features of optimization problems - Objective function-Continuous functions - Discrete functions - Unimodal functions - Convex and concave functions, Investment costs and operating costs in objective function - Optimizing profitably constraints-Internal and external constraints-Formulation of optimization problems. Continuous functions - Discrete functions - Unimodal functions - Convex and concave functions.	<b>07</b>	<b>15%</b>

<b>FIRST INTERNAL EXAM</b>			
<b>III</b>	Necessary and sufficient conditions for optimum of unconstrained functions-Numerical methods for unconstrained functions - One-dimensional search - Gradient-free search with fixed step size. Linear Programming - Basic concepts of linear programming - Graphical interpretation-Simplex method - Apparent difficulties in the Simplex method.	<b>06</b>	<b>15%</b>
<b>IV</b>	Transportation Problem, Loops in transportation table, Methods of finding initial basic feasible solution, Tests for optimality. Assignment Problem, Mathematical form of assignment problem, methods of solution.	<b>06</b>	<b>15%</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	Network analysis by linear programming and shortest route, maximal flow problem. Introduction to Non-traditional optimization, Computational Complexity – NP-Hard, NP-Complete. Tabu Search-Basic Tabu search, Neighborhood, Candidate list, Short term and Long term memory	<b>07</b>	<b>20%</b>
<b>VI</b>	Genetic Algorithms- Basic concepts, Encoding, Selection, Crossover, Mutation. Simulated Annealing - Acceptance probability, Cooling, Neighborhoods, Cost function. Application of GA and Simulated Annealing in solving sequencing and scheduling problems and Travelling salesman problem.	<b>08</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

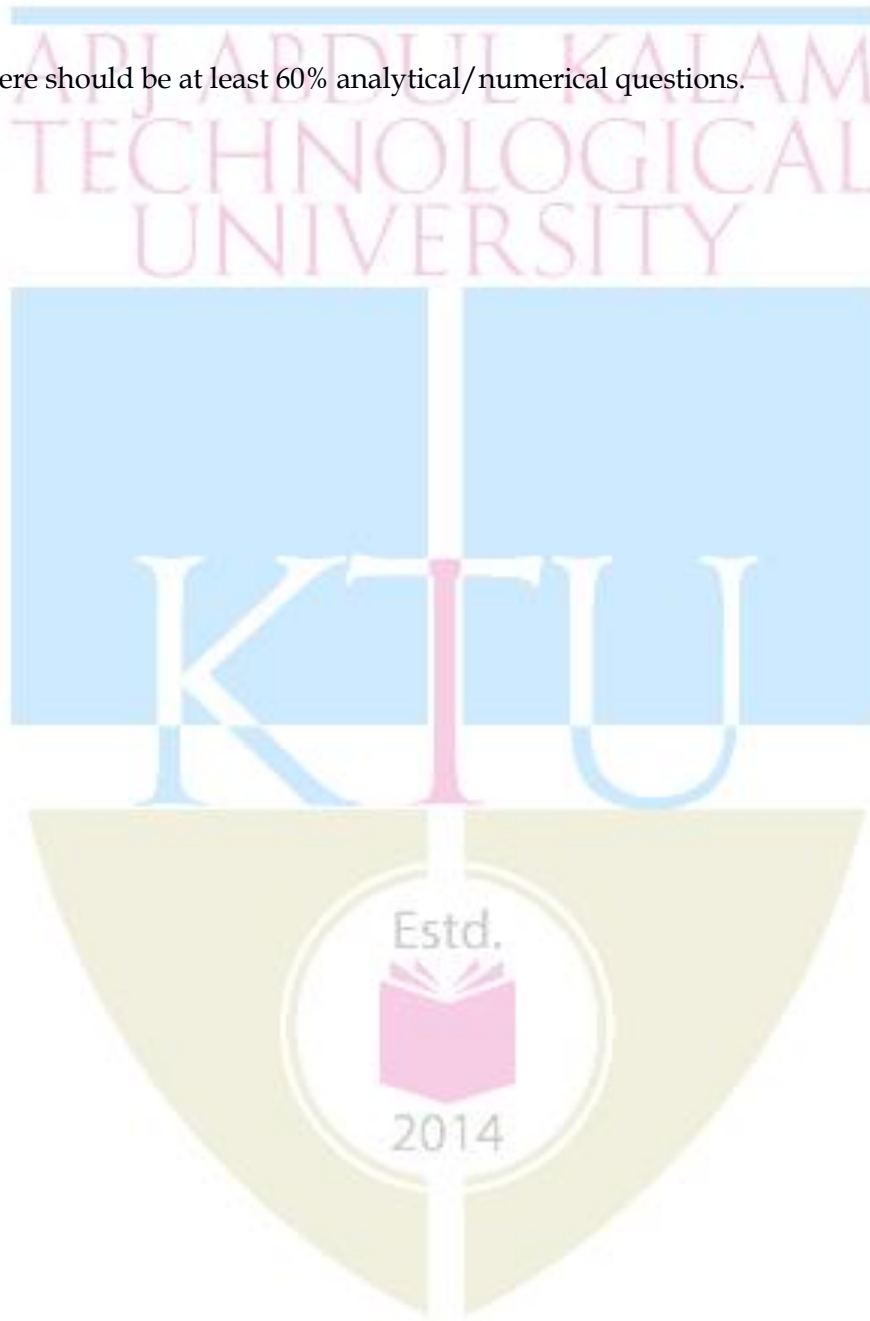
### Question Paper Pattern

1. There will be *five* parts in the question paper – A, B, C, D, E
2. Part A
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules I and II; Allfour questions have to be answered.
3. Part B
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three subparts.
4. Part C
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules III and IV; Allfour questions have to be answered.
5. Part D
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts

6. Part E

- a. Total Marks: 40
- b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
- c. A question can have a maximum of three sub-parts.

7. There should be at least 60% analytical/numerical questions.



Course code	Course Name	L-T-P - Credits	Year of Introduction
CS367	Logic for Computer Science	3-0-0-3	2016
<b>Pre-requisites :</b> CS205 Data Structures			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>To introduce the concepts of mathematical logic and its importance.</li> <li>To discuss propositional, predicate, temporal and modal logic and their applications.</li> </ul>			
<b>Syllabus</b>			
Propositional Logic, Resolution, binary decision diagrams, Predicate logic, resolution, temporal logic, deduction, program verification, modal logic.			
<b>Expected Outcome</b>			
The students will be able to			
<ol style="list-style-type: none"> <li>Gain the concept of logic and its importance.</li> <li>Understand fundamental concepts in propositional, predicate and temporal logic and apply resolution techniques.</li> <li>Apply the concept of program verification in real-world scenarios.</li> <li>Know the fundamental concepts in modal logic.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>Arindhama Singh, Logics for Computer Science, Prentice Hall India, 2004.</li> <li>Modechai Ben-Ari, Mathematical Logic for Computer Science, Springer, 3/e, 2012.</li> </ol>			
<b>Reference</b>			
<ol style="list-style-type: none"> <li>Michael Huth, Mark Ryan, Logic in Computer Science: Modeling and Reasoning about Systems, Cambridge University Press, 2005.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
I	Introductory Concepts: Mathematical Logic, Propositional Logic, First Order Logic, Modal and Temporal logic, Program Verification. <b>(Reading: Ben-Ari, Chapter 1)</b> Propositional Logic: Formulae and interpretations, Equivalence, Satisfiability & Validity, Semantic Tableaux, Soundness and Completeness. <b>(Reading: Ben-Ari, Chapter 2 except 2.4, Additional Reading : Singh, Chapter 1)</b>	06	15%
II	The Hilbert Deductive System, Derived Rules, Theorems and operators, Soundness and Completeness, Consistency. <b>(Reading: Ben-Ari, Chapter 3 except 3.7 and 3.8, Additional Reading : Singh, Chapter 1)</b> Resolution in Propositional Logic: Conjunctive Normal form, Clausal form, resolution rule. <b>(Reading: Ben-Ari, Chapter 4.1, 4.2, 4.3, Additional Reading : Singh, Chapter 1)</b>	06	15%
<b>FIRST INTERNAL EXAM</b>			
III	Binary Decision Diagrams: Definition, Reduced and ordered BDD, Operators. <b>(Reading: Ben-Ari, Chapter 5.1 – 5.5)</b> Predicate Logic: Relations, predicates, formulae and interpretation, logical equivalence, semantic tableaux, soundness. <b>Reading: Ben-Ari, Chapter 7.1-7.6, Additional Reading : Singh, Chapter 2)</b>	07	15%

<b>IV</b>	The Hilbert deduction system for predicate logic. Functions, PCNF and clausal form, Herbrand model. Resolution in predicate logic: ground resolution, substitution, unification, general resolution. <b>Reading: Ben-Ari, Chapter 8.1-8.4, 9.1, 9.3, 10.1-10.4, Additional Reading : Singh, Chapter 2, Chapter 3)</b>	<b>08</b>	<b>15%</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	Temporal logic: Syntax and semantics, models of time, linear time temporal logic, semantic tableaux. Deduction system of temporal logic. <b>(Reading: Ben-Ari, Chapter 13.1-13.5, 14.1-14.2)</b>	<b>07</b>	<b>20%</b>
<b>VI</b>	Program Verification: Need for verification, Framework for verification, Verification of sequential programs, deductive system, verification, synthesis. <b>(Reading: Ben-Ari, Chapter 15.1-15.4, Additional Reading : Singh, Chapter 5)</b> <b>Modal Logic:</b> Need for modal logic, Case Study: Syntax and Semantics of K, Axiomatic System KC, <b>(Reading: Singh, Chapter 6.1-6.3)</b>	<b>08</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

**Assignments:** Some of the assignments can be given on an interactive theorem prover like Isabelle or Coq.

#### Question Paper Pattern

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3. Part B
  - a. Total marks : 18
  - b. *Three* questions each having 9 marks, uniformly covering modules I and II; *Two* questions have to be answered. Each question can have a maximum of three subparts.
4. Part C
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  - b. *Four* questions each having 3 marks, uniformly covering modules III and IV; *Allfour* questions have to be answered.
5. Part D
  - a. Total marks : 18
  - b. *Three* questionseach having 9 marks, uniformly covering modules III and IV; *Two* questions have to be answered. Each question can have a maximum of three subparts
6. Part E
  - a. Total Marks: 40
  - b. *Six* questions each carrying 10 marks, uniformly covering modules V and VI; *four* questions have to be answered.
  - c. A question can have a maximum of three sub-parts.

There should be at least 60% analytical/numerical questions.

Course code	Course Name	L-T-P - Credits	Year of Introduction
CS369	Digital System Testing & Testable Design	3-0-0-3	2016
<b>Pre-requisites :</b> CS234 Digital Systems Lab			
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To expose the students to the basics of digital testing techniques applied to VLSI circuits.</li> <li>To introduce the concepts of algorithm development for automatic test pattern generation for digital circuits.</li> <li>To discuss fundamentals of design for testability.</li> </ul>			
<b>Syllabus</b> Basic terminology used in testing - functional and structural models of digital systems -logic simulation for design verification and testing-fault modeling - fault simulation - testing for faults - design for testability.			
<b>Expected Outcome</b> The students will be able to <ol style="list-style-type: none"> <li>Appreciate the basics of VLSI testing and functions modeling of circuits.</li> <li>Apply fault modeling using single stuck &amp; multiple stuck modeling for combinational circuits.</li> <li>Evaluate different methods for logic and fault simulations.</li> <li>Generate test patterns using automatic test pattern generation methods like D, PODEM &amp; FAN algorithms for combinational circuits.</li> <li>Explain automatic test pattern generation using time frame expansion and simulation based method for sequential circuits.</li> <li>Design digital circuits using scan path and self tests.</li> </ol>			
<b>Text Books</b> <ol style="list-style-type: none"> <li>Alexander Miczo, Digital Logic Testing and Simulation, Wiley, 2e, 2003.</li> <li>Michael L. Bushnell and Vishwani D. Agrawal, Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits, Springer, 2002.</li> <li>Miron Abramovici, Melvin A. Breuer, Arthur D. Friedman, Digital Systems Testing and Testable Design, Jaico Publishers, 2006.</li> </ol>			
<b>Reference</b> <ol style="list-style-type: none"> <li>Zainalabedin Navabi, Digital System test and testable design, Springer, 2011.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
I	<b>Fundamentals of Testing:</b> Testing & Diagnosis, testing at different levels of abstraction, errors & faults, modeling & evaluation, types of testing, test generation <b>Modeling:</b> Functional modeling at logic level, functional modeling at register level & structural models.	06	15%
II	<b>Fault Modeling :</b> Logic fault models, Fault detection and redundancy, Fault equivalence & fault location, fault dominance, single stuck faults, multiple stuck fault models .	06	15%
<b>FIRST INTERNAL EXAM</b>			

<b>III</b>	<b>Logic &amp; fault Simulation:</b> Simulation for verification & test evaluation, types of simulation – compiled code & Event driven, serial fault simulation, statistical method for fault simulation.	<b>07</b>	<b>15%</b>
<b>IV</b>	<b>Combinational circuit test generation:</b> ATG for SSFs in combinational circuits – fault oriented ATG- fault independent ATG- random test generation, Sensitized path, D-algorithm, PODEM and FAN.	<b>07</b>	<b>15%</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	<b>Sequential circuit test generation:</b> ATPG for single clock synchronous circuits, time frame expansion method, simulation based sequential circuit ATPG – genetic algorithm.	<b>07</b>	<b>20%</b>
<b>VI</b>	<b>Design for Testability:</b> introduction to testability, design for testability techniques, controllability and observability by means of scan registers, generic scan based designs – scan path, boundary scan, Introduction to BIST.	<b>09</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

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  - a. Total marks : 18
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6. Part E
  - a. Total Marks: 40
  - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
  - c. A question can have a maximum of three sub-parts.
7. There should be at least 60% analytical/numerical/design questions.

Course code	Course Name	L-T-P - Credits	Year of Introduction						
**341	DESIGN PROJECT	0-1-2-2	2016						
<b>Prerequisite : Nil</b>									
<p><b>Course Objectives</b></p> <ul style="list-style-type: none"> <li>• To understand the engineering aspects of design with reference to simple products</li> <li>• To foster innovation in design of products, processes or systems</li> <li>• To develop design that add value to products and solve technical problems</li> </ul>									
<p><b>Course Plan</b></p> <p><b>Study :</b>Take minimum three simple products, processes or techniques in the area of specialisation, study, analyse and present them. The analysis shall be focused on functionality, strength, material, manufacture/construction, quality, reliability, aesthetics, ergonomics, safety, maintenance, handling, sustainability, cost etc. whichever are applicable. Each student in the group has to present individually; choosing different products, processes or techniques.</p> <p><b>Design:</b> The project team shall identify an innovative product, process or technology and proceed with detailed design. At the end, the team has to document it properly and present and defend it. The design is expected to concentrate on functionality, design for strength is not expected.</p> <p><i>Note :</i> The one hour/week allotted for tutorial shall be used for discussions and presentations. The project team (not exceeding four) can be students from different branches, if the design problem is multidisciplinary.</p>									
<p><b>Expected outcome.</b></p> <p>The students will be able to</p> <ol style="list-style-type: none"> <li>i. Think innovatively on the development of components, products, processes or technologies in the engineering field</li> <li>ii. Analyse the problem requirements and arrive workable design solutions</li> </ol>									
<p><b>Reference:</b></p> <p>Michael Luchs, Scott Swan, Abbie Griffin, 2015. Design Thinking. 405 pages, John Wiley &amp; Sons, Inc</p>									
<p><b>Evaluation</b></p> <table style="width: 100%; border: none;"> <tr> <td style="width: 70%;">First evaluation ( Immediately after first internal examination )</td> <td style="text-align: right;">20 marks</td> </tr> <tr> <td>Second evaluation ( Immediately after second internal examination)</td> <td style="text-align: right;">20 marks</td> </tr> <tr> <td>Final evaluation ( Last week of the semester)</td> <td style="text-align: right;">60 marks</td> </tr> </table> <p><i>Note:</i> All the three evaluations are mandatory for course completion and for awarding the final grade.</p>				First evaluation ( Immediately after first internal examination )	20 marks	Second evaluation ( Immediately after second internal examination)	20 marks	Final evaluation ( Last week of the semester)	60 marks
First evaluation ( Immediately after first internal examination )	20 marks								
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Course code	Course Name	L-T-P - Credits	Year of Introduction
**352	Comprehensive Examination	0-1-1-2	2016
<b>Prerequisite : Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>To assess the comprehensive knowledge gained in basic courses relevant to the branch of study</li> <li>To comprehend the questions asked and answer them with confidence.</li> </ul>			
<b>Assessment</b>			
<p><b>Oral examination</b> – To be conducted by the college (@ three students/hour) covering all the courses up to and including V semester– 50 marks</p> <p><b>Written examination</b> - To be conducted by the Dept. on the date announced by the University– common to all students of the same branch – objective type ( 1 hour duration)– 50 multiple choice questions ( 4 choices) of 1 mark each covering the six common courses of S1&amp;S2 and six branch specific courses listed – questions are set by the University - no negative marks – 50 marks.</p> <p><i>Note:</i> Both oral and written examinations are mandatory. But separate minimum marks is not insisted for pass. If a students does not complete any of the two assessments, grade I shall be awarded and the final grade shall be given only after the completion of both the assessments. The two hours allotted for the course may be used by the students for discussion, practice and for oral assessment.</p>			
<b>Expected outcome.</b>			
<ul style="list-style-type: none"> <li>The students will be confident in discussing the fundamental aspects of any engineering problem/situation and give answers in dealing with them</li> </ul>			



Course code	Course Name	L-T-P - Credits	Year of Introduction
<b>**451</b>	<b>Seminar and Project Preliminary</b>	<b>0-1-4-2</b>	<b>2016</b>
<b>Prerequisite : Nil</b>			
<p><b>Course Objectives</b></p> <ul style="list-style-type: none"> <li>• To develop skills in doing literature survey, technical presentation and report preparation.</li> <li>• To enable project identification and execution of preliminary works on final semester project</li> </ul>			
<p><b>Course Plan</b></p> <p><b>Seminar:</b> Each student shall identify a topic of current relevance in his/her branch of engineering, get approval of faculty concerned, collect sufficient literature on the topic, study it thoroughly, prepare own report and present in the class.</p> <p><b>Project preliminary:</b> Identify suitable project relevant to the branch of study. Form project team ( not exceeding four students). The students can do the project individually also. Identify a project supervisor. Present the project proposal before the assessment board (excluding the external expert) and get it approved by the board.</p> <p>The preliminary work to be completed: (1) Literature survey (2) Formulation of objectives (3) Formulation of hypothesis/design/methodology (4) Formulation of work plan (5) Seeking funds (6) Preparation of preliminary report</p> <p><b>Note:</b> The same project should be continued in the eighth semester by the same project team.</p>			
<p><b>Expected outcome.</b></p> <p>The students will be able to</p> <ol style="list-style-type: none"> <li>Analyse a current topic of professional interest and present it before an audience</li> <li>Identify an engineering problem, analyse it and propose a work plan to solve it.</li> </ol>			
<p><b>Evaluation</b></p> <p>Seminar : <b>50 marks</b> (Distribution of marks for the seminar is as follows: i. Presentation : 40% ii. Ability to answer questions : 30% &amp; iii. Report : 30%)</p> <p>Project preliminary : <b>50 marks</b>( Progress evaluation by the supervisor : 40% and progress evaluation by the assessment board excluding external expert : 60%. Two progress evaluations, mid semester and end semester, are mandatory.)</p> <p><b>Note:</b> All evaluations are mandatory for course completion and for awarding the final grade.</p>			

Course code	Course Name	Credits	Year of Introduction						
**492	PROJECT	6	2016						
<b>Prerequisite : Nil</b>									
<b>Course Objectives</b>									
<ul style="list-style-type: none"> <li>• To apply engineering knowledge in practical problem solving</li> <li>• To foster innovation in design of products, processes or systems</li> <li>• To develop creative thinking in finding viable solutions to engineering problems</li> </ul>									
<b>Course Plan</b>									
<p>In depth study of the topic assigned in the light of the preliminary report prepared in the seventh semester</p> <p>Review and finalization of the approach to the problem relating to the assigned topic</p> <p>Preparing a detailed action plan for conducting the investigation, including team work</p> <p>Detailed Analysis/Modelling/Simulation/Design/Problem Solving/Experiment as needed</p> <p>Final development of product/process, testing, results, conclusions and future directions</p> <p>Preparing a paper for Conference presentation/Publication in Journals, if possible</p> <p>Preparing a report in the standard format for being evaluated by the dept. assessment board</p> <p>Final project presentation and viva voce by the assessment board including external expert</p>									
<b>Expected outcome</b>									
<p>The students will be able to</p> <ul style="list-style-type: none"> <li>iii. Think innovatively on the development of components, products, processes or technologies in the engineering field</li> <li>iv. Apply knowledge gained in solving real life engineering problems</li> </ul>									
<b>Evaluation</b>									
<b>Maximum Marks : 100</b>									
<table style="width: 100%; border: none;"> <tr> <td style="width: 50%;">(i) Two progress assessments</td> <td style="width: 50%;">20% by the faculty supervisor(s)</td> </tr> <tr> <td>(ii) Final project report</td> <td>30% by the assessment board</td> </tr> <tr> <td>(iii) Project presentation and viva voce</td> <td>50% by the assessment board</td> </tr> </table>				(i) Two progress assessments	20% by the faculty supervisor(s)	(ii) Final project report	30% by the assessment board	(iii) Project presentation and viva voce	50% by the assessment board
(i) Two progress assessments	20% by the faculty supervisor(s)								
(ii) Final project report	30% by the assessment board								
(iii) Project presentation and viva voce	50% by the assessment board								
<p><i>Note:</i> All the three evaluations are mandatory for course completion and for awarding the final grade.</p>									

Estd.

2014

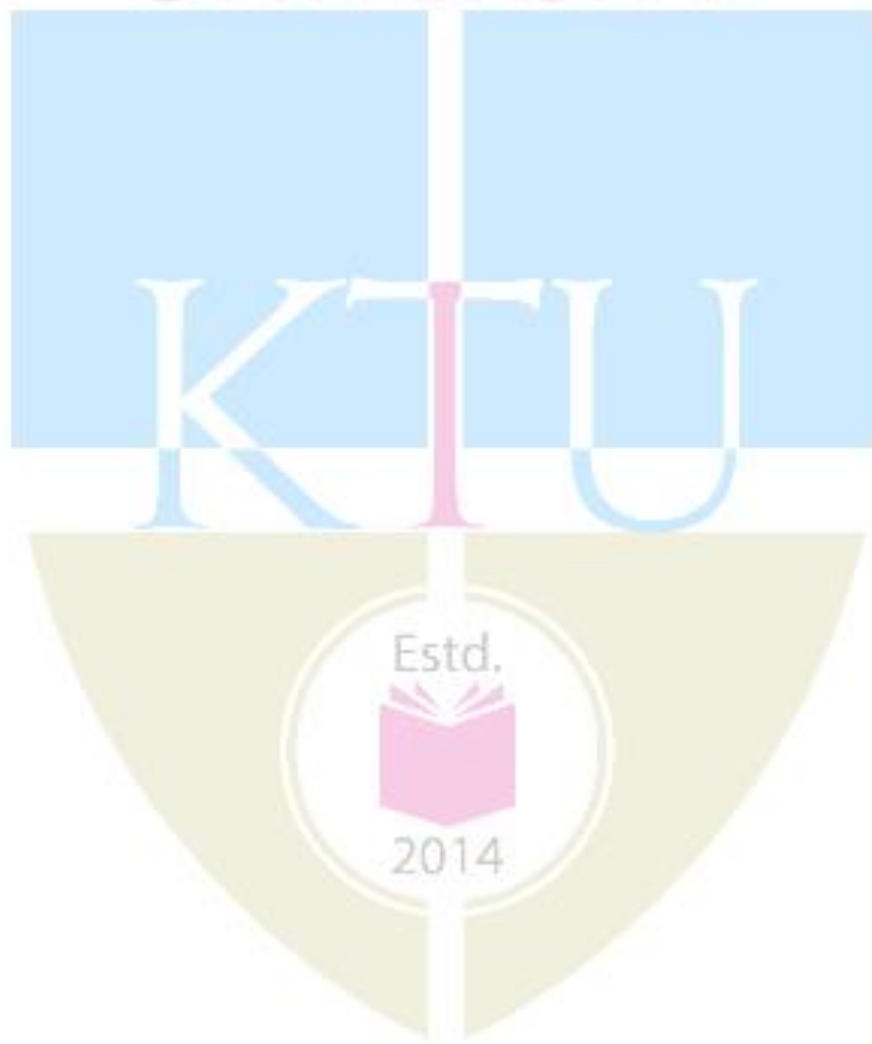
Course code	Course Name	L-T-P Credits	Year of Introduction
CS331	SYSTEM SOFTWARE LAB	0-0-3-1	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>To build an understanding on design and implementation of different types of system software.</li> </ul>			
<b>List of Exercises/Experiments: (Exercises/experiments marked with * are mandatory from each part. Total 12 Exercises/experiments are mandatory)</b>			
<i>Part A</i>			
<ol style="list-style-type: none"> <li>Simulate the following non-preemptive CPU scheduling algorithms to find turnaround time and waiting time.               <ol style="list-style-type: none"> <li>FCFS</li> <li>SJF</li> <li>Round Robin (pre-emptive)</li> <li>Priority</li> </ol> </li> <li>Simulate the following file allocation strategies.               <ol style="list-style-type: none"> <li>Sequential</li> <li>Indexed</li> <li>Linked</li> </ol> </li> <li>Implement the different paging techniques of memory management.</li> <li>Simulate the following file organization techniques *               <ol style="list-style-type: none"> <li>Single level directory</li> <li>Two level directory</li> <li>Hierarchical</li> </ol> </li> <li>Implement the banker's algorithm for deadlock avoidance.*</li> <li>Simulate the following disk scheduling algorithms. *               <ol style="list-style-type: none"> <li>FCFS</li> <li>SCAN</li> <li>C-SCAN</li> </ol> </li> <li>Simulate the following page replacement algorithms               <ol style="list-style-type: none"> <li>FIFO</li> <li>LRU</li> <li>LFU</li> </ol> </li> <li>Implement the producer-consumer problem using semaphores. *</li> <li>Write a program to simulate the working of the dining philosopher's problem.*</li> </ol>			
<i>Part B</i>			
<ol style="list-style-type: none"> <li>Implement the symbol table functions: create, insert, modify, search, and display.</li> <li>Implement pass one of a two pass assembler. *</li> <li>Implement pass two of a two pass assembler. *</li> <li>Implement a single pass assembler. *</li> <li>Implement a two pass macro processor *</li> <li>Implement a single pass macro processor.</li> <li>Implement an absolute loader.</li> <li>Implement a relocating loader.</li> <li>Implement pass one of a direct-linking loader.</li> <li>Implement pass two of a direct-linking loader.</li> <li>Implement a simple text editor with features like insertion / deletion of a character, word, and sentence.</li> <li>Implement a symbol table with suitable hashing.*</li> </ol>			

**Expected Outcome**

The students will be able to

- i. Compare and analyze CPU Scheduling Algorithms like FCFS, Round Robin, SJF, and Priority.
- ii. Implement basic memory management schemes like paging.
- iii. Implement synchronization techniques using semaphores etc.
- iv. Implement banker's algorithm for deadlock avoidance.
- v. Implement memory management schemes and page replacement schemes and file allocation and organization techniques.
- vi. Implement system software such as loaders, assemblers and macro processor.

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Course code	Course Name	L-T-P - Credits	Year of Introduction
CS333	APPLICATION SOFTWARE DEVELOPMENT LAB	0-0-3-1	2016
<b>Pre-requisite : CS208 Principles of Database Design</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>• To introduce basic commands and operations on database.</li> <li>• To introduce stored programming concepts (PL-SQL) using Cursors and Triggers .</li> <li>• To familiarize front end tools of database.</li> </ul>			
<b>List of Exercises/Experiments: (Exercises/experiments marked with * are mandatory. Total 12 Exercises/experiments are mandatory)</b>			
<ol style="list-style-type: none"> <li>1. Creation of a database using DDL commands and writes DQL queries to retrieve information from the database.</li> <li>2. Performing DML commands like Insertion, Deletion, Modifying, Altering, and Updating records based on conditions.</li> <li>3. Creating relationship between the databases. *</li> <li>4. Creating a database to set various constraints. *</li> <li>5. Practice of SQL TCL commands like Rollback, Commit, Savepoint.</li> <li>6. Practice of SQL DCL commands for granting and revoking user privileges.</li> <li>7. Creation of Views and Assertions *</li> <li>8. Implementation of Build in functions in RDBMS *</li> <li>9. Implementation of various aggregate functions in SQL *</li> <li>10. Implementation of Order By, Group By &amp; Having clause. *</li> <li>11. Implementation of set operators, nested queries and Join queries *</li> <li>12. Implementation of various control structures using PL/SQL *</li> <li>13. Creation of Procedures and Functions *</li> <li>14. Creation of Packages *</li> <li>15. Creation of database Triggers and Cursors *</li> <li>16. Practice various front-end tools and report generation.</li> <li>17. Creating Forms and Menus</li> <li>18. Mini project (Application Development using Oracle/ MySQL using Database connectivity)* <ol style="list-style-type: none"> <li>a. Inventory Control System.</li> <li>b. Material Requirement Processing.</li> <li>c. Hospital Management System.</li> <li>d. Railway Reservation System.</li> <li>e. Personal Information System.</li> <li>f. Web Based User Identification System.</li> <li>g. Timetable Management System.</li> <li>h. Hotel Management System.</li> </ol> </li> </ol>			
<b>Expected Outcome</b>			
The students will be able to			
<ol style="list-style-type: none"> <li>i. Design and implement a database for a given problem using database design principles.</li> <li>ii. Apply stored programming concepts (PL-SQL) using Cursors and Triggers.</li> <li>iii. Use graphical user interface, Event Handling and Database connectivity to develop and deploy applications and applets.</li> <li>iv. Develop medium-sized project in a team.</li> </ol>			

Course code	Course Name	L-T-P - Credits	Year of Introduction
CS302	Design and Analysis of Algorithms	3-1-0-4	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>• To introduce the concepts of Algorithm Analysis, Time Complexity, Space Complexity.</li> <li>• To discuss various Algorithm Design Strategies with proper illustrative examples.</li> <li>• To introduce Complexity Theory.</li> </ul>			
<b>Syllabus</b>			
Introduction to Algorithm Analysis, Notions of Time and Space Complexity, Asymptotic Notations, Recurrence Equations and their solutions, Master's Theorem, Divide and Conquer and illustrative examples, AVL trees, Red-Black Trees, Union-find algorithms, Graph algorithms, Divide and Conquer, Dynamic Programming, Greedy Strategy, Back Tracking and Branch and Bound, Complexity classes			
<b>Expected outcome</b>			
The students will be able to			
<ol style="list-style-type: none"> <li>i. Analyze a given algorithm and express its time and space complexities in asymptotic notations.</li> <li>ii. Solve recurrence equations using Iteration Method, Recurrence Tree Method and Master's Theorem.</li> <li>iii. Design algorithms using Divide and Conquer Strategy.</li> <li>iv. Compare Dynamic Programming and Divide and Conquer Strategies.</li> <li>v. Solve Optimization problems using Greedy strategy.</li> <li>vi. Design efficient algorithms using Back Tracking and Branch Bound Techniques for solving problems.</li> <li>vii. Classify computational problems into P, NP, NP-Hard and NP-Complete.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>1. Ellis Horowitz, SartajSahni, SanguthevarRajasekaran, Computer Algorithms, Universities Press, 2007 [Modules 3,4,5]</li> <li>2. Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest, Clifford Stein, Introduction to Algorithms, MIT Press, 2009 [Modules 1,2,6]</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>1. Alfred V. Aho, John E. Hopcroft and Jeffrey D. Ullman, The Design and Analysis of Computer Algorithms, Pearson Education, 1999.</li> <li>2. Anany Levitin, Introduction to the Design and Analysis of Algorithms, Pearson, 3rd Edition, 2011.</li> <li>3. Gilles Brassard, Paul Bratley, Fundamentals of Algorithmics, Pearson Education, 1995.</li> <li>4. Richard E. Neapolitan, Kumarss Naimipour, Foundations of Algorithms using C++ Psuedocode, Second Edition, 1997.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks

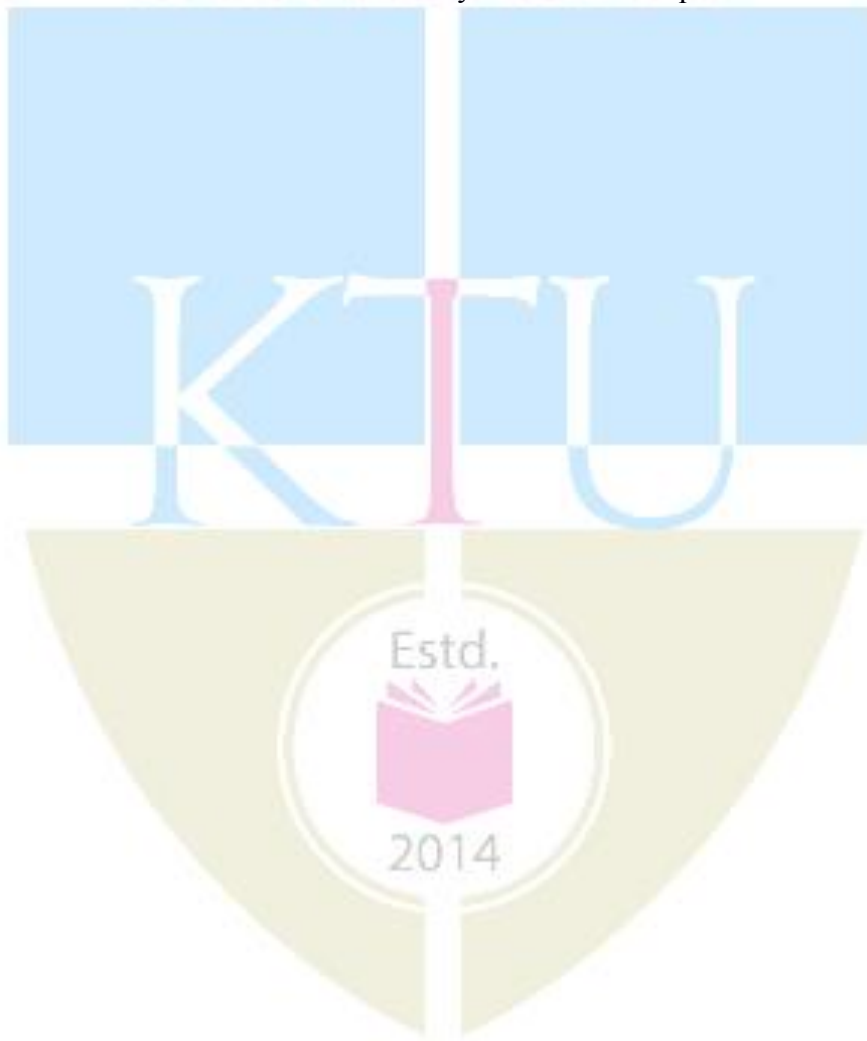
I	<b>Introduction to Algorithm Analysis</b> Time and Space Complexity- Elementary operations and Computation of Time Complexity- Best, worst and Average Case Complexities- Complexity Calculation of simple algorithms <b>Recurrence Equations:</b> Solution of Recurrence Equations – Iteration Method and Recursion Tree Methods	04 04	15 %
II	<b>Master's Theorem</b> (Proof not required) – examples, Asymptotic Notations and their properties- Application of Asymptotic Notations in Algorithm Analysis- Common Complexity Functions <b>AVL Trees</b> – rotations, Red-Black Trees insertion and deletion (Techniques only; algorithms not expected). B-Trees – insertion and deletion operations. Sets- Union and find operations on disjoint sets.	05 05	15%
<b>FIRST INTERNAL EXAM</b>			
III	<b>Graphs</b> – DFS and BFS traversals, complexity, Spanning trees – Minimum Cost Spanning Trees, single source shortest path algorithms, Topological sorting, strongly connected components.	07	15%
IV	<b>Divide and Conquer:</b> The Control Abstraction, 2 way Merge sort, Strassen's Matrix Multiplication, Analysis <b>Dynamic Programming</b> : The control Abstraction- The Optimality Principle- Optimal matrix multiplication, Bellman-Ford Algorithm	04 05	15%
<b>SECOND INTERNAL EXAM</b>			
V	Analysis, Comparison of Divide and Conquer and Dynamic Programming strategies <b>Greedy Strategy:</b> - The Control Abstraction- the Fractional Knapsack Problem, Minimal Cost Spanning Tree Computation- Prim's Algorithm – Kruskal's Algorithm.	02 04 03	20%
VI	<b>Back Tracking:</b> -The Control Abstraction – The N Queen's Problem, 0/1 Knapsack Problem <b>Branch and Bound:</b> Travelling Salesman Problem. <b>Introduction to Complexity Theory</b> :-Tractable and Intractable Problems- The P and NP Classes- Polynomial Time Reductions - The NP- Hard and NP-Complete Classes	03 03 03	20%
<b>END SEMESTER EXAM</b>			

### Question Paper Pattern

1. There will be *five* parts in the question paper – A, B, C, D, E
2. Part A
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules I and II; All four questions have to be answered.
3. Part B
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three subparts.
4. Part C



- a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules III and IV; Allfour questions have to be answered.
5. Part D
- a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
6. Part E
- a. Total Marks: 40
  - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
  - c. A question can have a maximum of three sub-parts.
7. There should be at least 60% analytical/numerical questions.



Course code	Course Name	L-T-P Credits	Year of Introduction
CS304	COMPILER DESIGN	3-0-0-3	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>To provide a thorough understanding of the internals of Compiler Design.</li> </ul>			
<b>Syllabus</b>			
Phases of compilation, Lexical analysis, Token Recognition, Syntax analysis, Bottom Up and Top Down Parsers, Syntax directed translation schemes, Intermediate Code Generation, Triples and Quadruples, Code Optimization, Code Generation.			
<b>Expected Outcome</b>			
The students will be able to			
<ol style="list-style-type: none"> <li>Explain the concepts and different phases of compilation with compile time error handling.</li> <li>Represent language tokens using regular expressions, context free grammar and finite automata and design lexical analyzer for a language.</li> <li>Compare top down with bottom up parsers, and develop appropriate parser to produce parse tree representation of the input.</li> <li>Generate intermediate code for statements in high level language.</li> <li>Design syntax directed translation schemes for a given context free grammar.</li> <li>Apply optimization techniques to intermediate code and generate machine code for high level language program.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>Aho A. Ravi Sethi and D Ullman. Compilers – Principles Techniques and Tools, Addison Wesley, 2006.</li> <li>D. M.Dhamdhare, System Programming and Operating Systems, Tata McGraw Hill &amp; Company, 1996.</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>Kenneth C. Louden, Compiler Construction – Principles and Practice, Cengage Learning Indian Edition, 2006.</li> <li>Tremblay and Sorenson, The Theory and Practice of Compiler Writing, Tata McGraw Hill &amp; Company, 1984.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
<b>I</b>	Introduction to compilers – Analysis of the source program, Phases of a compiler, Grouping of phases, compiler writing tools – bootstrapping <b>Lexical Analysis:</b> The role of Lexical Analyzer, Input Buffering, Specification of Tokens using Regular Expressions, Review of Finite Automata, Recognition of Tokens.	<b>07</b>	<b>15%</b>
<b>II</b>	<b>Syntax Analysis:</b> Review of Context-Free Grammars – Derivation trees and Parse Trees, Ambiguity. <b>Top-Down Parsing:</b> Recursive Descent parsing, Predictive parsing, LL(1) Grammars.	<b>06</b>	<b>15%</b>

<b>FIRST INTERNAL EXAM</b>			
<b>III</b>	<b>Bottom-Up Parsing:</b> Shift Reduce parsing – Operator precedence parsing (Concepts only) LR parsing – Constructing SLR parsing tables, Constructing, Canonical LR parsing tables and Constructing LALR parsing tables.	<b>07</b>	<b>15%</b>
<b>IV</b>	<b>Syntax directed translation:</b> Syntax directed definitions, Bottom- up evaluation of S-attributed definitions, L- attributed definitions, Top-down translation, Bottom-up evaluation of inherited attributes. <b>Type Checking :</b> Type systems, Specification of a simple type checker.	<b>08</b>	<b>15%</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	<b>Run-Time Environments:</b> Source Language issues, Storage organization, Storage-allocation strategies. <b>Intermediate Code Generation (ICG):</b> Intermediate languages – Graphical representations, Three-Address code, Quadruples, Triples. Assignment statements, Boolean expressions.	<b>07</b>	<b>20%</b>
<b>VI</b>	<b>Code Optimization:</b> Principal sources of optimization, Optimization of Basic blocks <b>Code generation:</b> Issues in the design of a code generator. The target machine, A simple code generator.	<b>07</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

### Question Paper Pattern

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2. Part A
  - a. Total marks : 12      b. *Four* questions each having 3 marks, uniformly covering modules I and II; *All four* questions have to be answered.
3. Part B
  - a. Total marks : 18      b. *Three* question each having 9 marks, uniformly covering modules I and II; *Two* questions have to be answered. Each question can have a maximum of three subparts.
4. Part C
  - a. Total marks : 12      b. *Four* questions each having 3 marks, uniformly covering modules III and IV; *All four* questions have to be answered.
5. Part D
  - a. Total marks : 18      b. *Three* questions each having 9 marks, uniformly covering modules III and IV; *Two* questions have to be answered. Each question can have a maximum of three subparts
6. Part E
  - b. Total Marks: 40      b. *Six* questions each carrying 10 marks, uniformly covering modules V and VI; *four* questions have to be answered.
  - c. A question can have a maximum of three sub-parts.
7. There should be at least 60% analytical/numerical questions.

Course code	Course Name	L-T-P - Credits	Year of Introduction
CS306	Computer Networks	3-0-0-3	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>• To build an understanding of the fundamental concepts of computer networking.</li> <li>• To introduce the basic taxonomy and terminology of computer networking.</li> <li>• To introduce advanced networking concepts.</li> </ul>			
<b>Syllabus</b>			
Concept of layering, LAN technologies (Ethernet), Flow and error control techniques, switching, IPv4/IPv6, routers and routing algorithms (distance vector, link state), TCP/UDP and sockets, congestion control, Application layer protocols.			
<b>Expected Outcome</b>			
The students will be able to			
<ol style="list-style-type: none"> <li>i. Visualise the different aspects of networks, protocols and network design models.</li> <li>ii. Examine various Data Link layer design issues and Data Link protocols.</li> <li>iii. Analyse and compare different LAN protocols.</li> <li>iv. Compare and select appropriate routing algorithms for a network.</li> <li>v. Examine the important aspects and functions of network layer, transport layer and application layer in internetworking.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>1. Andrew S. Tanenbaum, Computer Networks, 4/e, PHI.</li> <li>2. Behrouz A. Forouzan, Data Communications and Networking, 4/e, Tata McGraw Hill.</li> <li>3. Larry L. Peterson &amp; Bruce S. Dave, Computer Networks-A Systems Approach, 5/e, Morgan Kaufmann, 2011.</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>1. Fred Halsall, Computer Networking and the Internet, 5/e.</li> <li>2. James F. Kurose, Keith W. Ross, Computer Networking: A Top-Down Approach, 6/e.</li> <li>3. Keshav, An Engineering Approach to Computer Networks, Addison Wesley, 1998.</li> <li>4. Request for Comments (RFC) Pages - IETF -<a href="https://www.ietf.org/rfc.html">https://www.ietf.org/rfc.html</a></li> <li>5. W. Richard Stevens. TCP/IP Illustrated volume 1, Addison-Wesley, 2005.</li> <li>6. William Stallings, Computer Networking with Internet Protocols, Prentice-Hall, 2004.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
<b>I</b>	Introduction – Uses – Network Hardware – LAN –MAN – WAN, Internetworks – Network Software – Protocol hierarchies – Design issues for the layers – Interface & Service – Service Primitives. Reference models – OSI – TCP/IP.	<b>07</b>	<b>15%</b>
<b>II</b>	Data Link layer Design Issues – Flow Control and ARQ techniques. Data link Protocols – HDLC. DLL in Internet. MAC Sub layer – IEEE 802 FOR LANs & MANs, IEEE 802.3, 802.4, 802.5. Bridges - Switches – High Speed LANs - Gigabit Ethernet. Wireless LANs - 802.11 a/b/g/n, 802.15.PPP	<b>08</b>	<b>15%</b>
<b>FIRST INTERNAL EXAMINATION</b>			

<b>III</b>	Network layer – Routing – Shortest path routing, Flooding, Distance Vector Routing, Link State Routing, RIP, OSPF, Routing for mobile hosts.	<b>07</b>	<b>15%</b>
<b>IV</b>	Congestion control algorithms – QoS. Internetworking – Network layer in internet. IPv4 - IP Addressing – Classless and Classfull Addressing. Sub-netting.	<b>07</b>	<b>15%</b>
<b>SECOND INTERNAL EXAMINATION</b>			
<b>V</b>	Internet Control Protocols – ICMP, ARP, RARP, BOOTP. Internet Multicasting – IGMP, Exterior Routing Protocols – BGP. IPv6 – Addressing – Issues, ICMPv6.	<b>07</b>	<b>20%</b>
<b>VI</b>	Transport Layer – TCP & UDP. Application layer –FTP, DNS, Electronic mail, MIME, SNMP. Introduction to World Wide Web.	<b>07</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

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  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three subparts.
4. Part C
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules III and IV; Allfour questions have to be answered.
5. Part D
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
6. Part E
  - a. Total Marks: 40
  - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
  - c. A question can have a maximum of three sub-parts.
7. There should be at least 60% analytical/numerical questions.

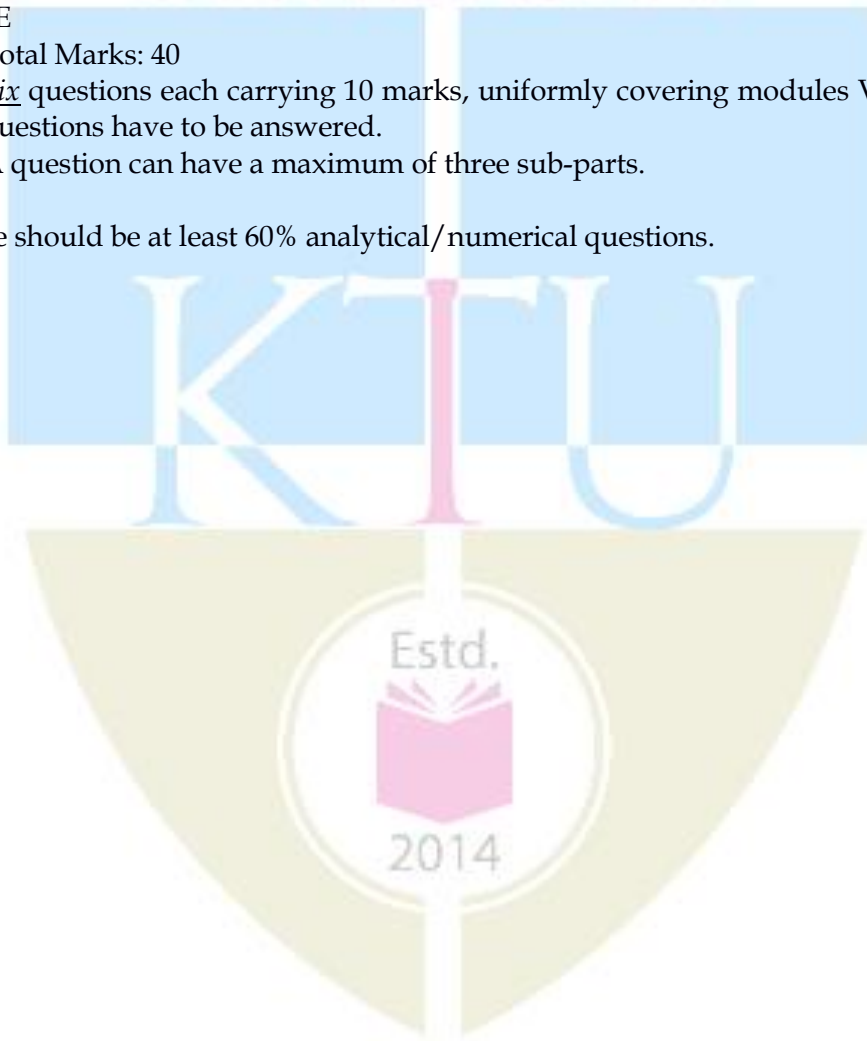
Course code	Course Name	L-T-P-Credits	Year of Introduction
CS308	Software Engineering and Project Management	3-0-0-3	2016
<b>Pre-requisite: Nil</b>			
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To introduce the fundamental concepts of software engineering.</li> <li>To build an understanding on various phases of software development.</li> <li>To introduce various software process models.</li> </ul>			
<b>Syllabus</b> Introduction to software engineering, Software process models, Software development phases, Requirement analysis, Planning, Design, Coding, Testing, Maintenance.			
<b>Expected Outcome</b> The students will be able to <ol style="list-style-type: none"> <li>Identify suitable life cycle models to be used.</li> <li>Analyze a problem and identify and define the computing requirements to the problem.</li> <li>Translate a requirement specification to a design using an appropriate software engineering methodology.</li> <li>Formulate appropriate testing strategy for the given software system.</li> <li>Develop software projects based on current technology, by managing resources economically and keeping ethical values.</li> </ol>			
<b>References</b> <ol style="list-style-type: none"> <li>Ian Sommerville, Software Engineering, University of Lancaster, Pearson Education, Seventh edition, 2004.</li> <li>K. K. Aggarwal and Yogesh Singh, Software Engineering, New age International Publishers, Second edition, 2005.</li> <li>Roger S. Pressman, Software Engineering : A practitioner's approach, McGraw Hill publication, Eighth edition, 2014</li> <li>S.A. Kelkar, Software Project Management: A concise study, PHI, Third edition, 2012.</li> <li>Walker Royce, Software Project Management : A unified frame work, Pearson Education, 1998</li> </ol>			
<b>COURSE PLAN</b>			
Module	Contents	Hours	End Sem. Exam Marks
I	Introduction to software engineering- scope of software	07	15%

	engineering - historical aspects, economic aspects, maintenance aspects, specification and design aspects, team programming aspects. Software engineering a layered technology - processes, methods and tools. Software process models - prototyping models, incremental models, spiral model, waterfall model.		
<b>II</b>	Process Framework Models: Capability maturity model (CMM), ISO 9000. Phases in Software development - requirement analysis- requirements elicitation for software, analysis principles, software prototyping, specification.	<b>06</b>	<b>15%</b>
<b>FIRST INTERNAL EXAM</b>			
<b>III</b>	Planning phase - project planning objective, software scope, empirical estimation models- COCOMO, single variable model, staffing and personal planning. Design phase - design process, principles, concepts, effective modular design, top down, bottom up strategies, stepwise refinement.	<b>07</b>	<b>15%</b>
<b>IV</b>	Coding - programming practice, verification, size measures, complexity analysis, coding standards. Testing - fundamentals, white box testing, control structure testing, black box testing, basis path testing, code walk-throughs and inspection, testing strategies-Issues, Unit testing, integration testing, Validation testing, System testing.	<b>07</b>	<b>15%</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	Maintenance-Overview of maintenance process, types of maintenance. Risk management: software risks - risk identification-risk monitoring and management. Project Management concept: People - Product-Process-Project.	<b>07</b>	<b>20%</b>
<b>VI</b>	Project scheduling and tracking: Basic concepts-relation between people and effort-defining task set for the software project-selecting software engineering task Software configuration management: Basics and standards User interface design - rules. Computer aided software engineering tools - CASE building blocks, taxonomy of CASE tools, integrated CASE environment.	<b>08</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

### Question Paper Pattern

1. There will be *five* parts in the question paper - A, B, C, D, E
2. Part A
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules I and II;

- All four questions have to be answered.
3. Part B
    - a. Total marks : 18
    - b. Three questions each having 9 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three subparts.
  4. Part C
    - a. Total marks : 12
    - b. Four questions each having 3 marks, uniformly covering modules III and IV; All four questions have to be answered.
  5. Part D
    - a. Total marks : 18
    - b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
  6. Part E
    - a. Total Marks: 40
    - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
    - c. A question can have a maximum of three sub-parts.
  7. There should be at least 60% analytical/numerical questions.





Course code	Course Name	L-T-P - Credits	Year of Introduction
HS300	Principles of Management	3-0-0-3	2016
<b>Prerequisite : Nil</b>			
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>To develop ability to critically analyse and evaluate a variety of management practices in the contemporary context;</li> <li>To understand and apply a variety of management and organisational theories in practice;</li> <li>To be able to mirror existing practices or to generate their own innovative management competencies, required for today's complex and global workplace;</li> <li>To be able to critically reflect on ethical theories and social responsibility ideologies to create sustainable organisations.</li> </ul>			
<b>Syllabus</b> Definition, roles and functions of a manager, management and its science and art perspectives, management challenges and the concepts like, competitive advantage, entrepreneurship and innovation. Early contributors and their contributions to the field of management. Corporate Social Responsibility. Planning, Organizing, Staffing and HRD functions, Leading and Controlling. Decision making under certainty, uncertainty and risk, creative process and innovation involved in decision making.			
<b>Expected outcome.</b> A student who has undergone this course would be able to <ol style="list-style-type: none"> <li>manage people and organisations</li> <li>critically analyse and evaluate management theories and practices</li> <li>plan and make decisions for organisations</li> <li>do staffing and related HRD functions</li> </ol>			
<b>Text Book:</b> Harold Koontz and Heinz Weirich, <i>Essentials of Management</i> , McGraw Hill Companies, 10th Edition.			
<b>References:</b> <ol style="list-style-type: none"> <li>Daft, <i>New era Management</i>, 11th Edition, Cengage Learning</li> <li>Griffin, <i>Management Principles and Applications</i>, 10th Edition, Cengage Learning</li> <li>Heinz Weirich, Mark V Cannice and Harold Koontz, <i>Management: a Global, Innovative and Entrepreneurial Perspective</i>, McGraw Hill Education, 14th Edition</li> <li>Peter F Drucker, <i>The Practice of Management</i>, McGraw Hill, New York</li> <li>Robbins and Coulter, <i>Management</i>, 13th Edition, 2016, Pearson Education</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	Sem. Exam Marks
I	Introduction to Management: definitions, managerial roles and functions; Science or Art perspectives- External environment-global, innovative and entrepreneurial perspectives of Management (3 Hrs.)– Managing people and organizations in the context of New Era- Managing for competitive advantage - the Challenges of Management (3 Hrs.)	6	15%

<b>II</b>	<b>Early Contributions and Ethics in Management:</b> Scientific Management- contributions of Taylor, Gilbreths, Human Relations approach-contributions of Mayo, McGregor's Theory, Ouchi's Theory Z (3 Hrs.) Systems Approach, the Contingency Approach, the Mckinsey 7-S Framework Corporate Social responsibility- Managerial Ethics. (3 Hrs)	6	15%
<b>FIRST INTERNAL EXAMINATION</b>			
<b>III</b>	<b>Planning:</b> Nature and importance of planning, -types of plans (3 Hrs.)- Steps in planning, Levels of planning - The Planning Process. – MBO (3 Hrs.).	6	15%
<b>IV</b>	<b>Organising for decision making:</b> Nature of organizing, organization levels and span of control in management Organisational design and structure –departmentation, line and staff concepts (3 Hrs.) Limitations of decision making- Evaluation and selecting from alternatives- programmed and non programmed decisions - decision under certainty, uncertainty and risk-creative process and innovation (3 Hrs.)	6	15%
<b>SECOND INTERNAL EXAMINATION</b>			
<b>V</b>	<b>Staffing and related HRD Functions:</b> definition, Empowerment, staff – delegation, decentralization and recentralisation of authority – Effective Organizing and culture-responsive organizations –Global and entrepreneurial organizing (3 Hrs.) Manager inventory chart-matching person with the job-system approach to selection (3 Hrs.) Job design-skills and personal characteristics needed in managers-selection process, techniques and instruments (3 Hrs.)	9	20%
<b>VI</b>	<b>Leading and Controlling:</b> Leading Vs Managing – Trait approach and Contingency approaches to leadership - Dimensions of Leadership (3 Hrs.) - Leadership Behavior and styles – Transactional and Transformational Leadership (3 Hrs.) Basic control process- control as a feedback system – Feed Forward Control – Requirements for effective control – control techniques – Overall controls and preventive controls – Global controlling (3 Hrs.)	9	20%
<b>END SEMESTER EXAM</b>			

### Question Paper Pattern

Max. marks: 100, Time: 3 hours .

The question paper shall consist of three parts

**Part A:** 4 questions uniformly covering modules I and II. Each question carries 10 marks

Students will have to answer any three questions out of 4 (3X10 marks =30 marks)

**Part B :** 4 questions uniformly covering modules III and IV. Each question carries 10 marks

Students will have to answer any three questions out of 4 (3X10 marks =30 marks)

**Part C:** 6 questions uniformly covering modules V and VI. Each question carries 10 marks

Students will have to answer any four questions out of 6 (4X10 marks =40 marks)

**Note:** In all parts, each question can have a maximum of four sub questions, if needed.

Course code	Course Name	L-T-P-Credits	Year of Introduction
CS362	Computer Vision	3-0-0-3	2016
<b>Pre-requisite: NIL</b>			
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To build an understanding on detailed models of image formation.</li> <li>• To expose the students to image feature detection and matching.</li> <li>• To introduce fundamental algorithms for pattern recognition.</li> <li>• To introduce various classification techniques.</li> <li>• To expose the students to various structural pattern recognition and feature extraction techniques.</li> </ul>			
<b>Syllabus</b> Image formation and Image model with Components of a vision system, Multiple images and the Geometry of multiple views, High level vision, Basics of pattern recognition, Linear discriminant based classifiers and tree classifiers, Unsupervised Methods, Recent Advances in Pattern Recognition.			
<b>Expected Outcome</b> The students will be able to <ol style="list-style-type: none"> <li>i. Appreciate the detailed models of image formation.</li> <li>ii. Analyse the techniques for image feature detection and matching.</li> <li>iii. Apply various algorithms for pattern recognition.</li> <li>iv. Examine various clustering algorithms.</li> <li>v. Analyze structural pattern recognition and feature extraction techniques.</li> </ol>			
<b>Text Books:</b> <ol style="list-style-type: none"> <li>1. Bernd Jahne and Horst HauBecker, Computer vision and Applications, Academic press, 2000.</li> <li>2. David A. Forsyth &amp; Jean Ponce, Computer vision – A Modern Approach, Prentice Hall, 2002.</li> </ol>			
<b>References</b> <ol style="list-style-type: none"> <li>1. C. M. Bishop, Pattern Recognition and Machine Learning, Springer, 2006.</li> <li>2. R. O. Duda, P. E. Hart and D. G. Stork, Pattern Classification, John Wiley, 2001.</li> <li>3. Richard Hartley and Andrew Zisserman, Multiple View Geometry in Computer Vision, Second Edition, Cambridge University Press, 2004.</li> <li>4. S. Theodoridis and K. Koutroumbas, Pattern Recognition, 4th Ed., Academic Press, 2009.</li> </ol>			
<b>COURSE PLAN</b>			
Module	Contents	Hours	End Sem. Exam Marks

I	Image formation and Image model- Components of a vision system- Cameras- camera model and camera calibration- Radiometry- Light in space- Light in surface - Sources, shadows and shading.	06	15%
II	Multiple images-The Geometry of multiple views- Stereopsis- Affine structure from motion- Elements of Affine Geometry Affine structure and motion from two images- Affine structure and motion from multiple images- From Affine to Euclidean images.	07	15%
<b>FIRST INTERNAL EXAM</b>			
III	High level vision- Geometric methods- Model based vision- Obtaining hypothesis by pose consistency, pose clustering and using Invariants, Verification.	07	15%
IV	Introduction to pattern and classification, supervised and unsupervised learning, Clustering Vs classification, Bayesian Decision Theory- Minimum error rate classification Classifiers, discriminant functions, decision surfaces- The normal density and discriminant-functions for the Normal density.	07	15%
<b>SECOND INTERNAL EXAM</b>			
V	<b>Linear discriminant based classifiers and tree classifiers</b> Linear discriminant function based classifiers- Perceptron- Minimum Mean Squared Error (MME) method, Support Vector machine, Decision Trees: CART, ID3.	07	20%
VI	<b>Unsupervised Methods</b> Basics of Clustering; similarity / dissimilarity measures; clustering criteria. Different distance functions and similarity measures, K-means algorithm. <b>Recent Advances in Pattern Recognition</b> Neural network structures for pattern recognition, Pattern classification using Genetic Algorithms.	08	20%
<b>END SEMESTER EXAM</b>			

### Question Paper Pattern

1. There will be *five* parts in the question paper – A, B, C, D, E
2. Part A
  - a. Total marks : 12
  - b. *Four* questions each having 3 marks, uniformly covering modules I and II; *Allfour* questions have to be answered.
3. Part B
  - a. Total marks : 18
  - b. *Three* questions each having 9 marks, uniformly covering modules I and II;

Two questions have to be answered. Each question can have a maximum of three subparts.

4. Part C

a. Total marks : 12

b. Four questions each having 3 marks, uniformly covering modules III and IV; Allfour questions have to be answered.

5. Part D

a. Total marks : 18

b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts

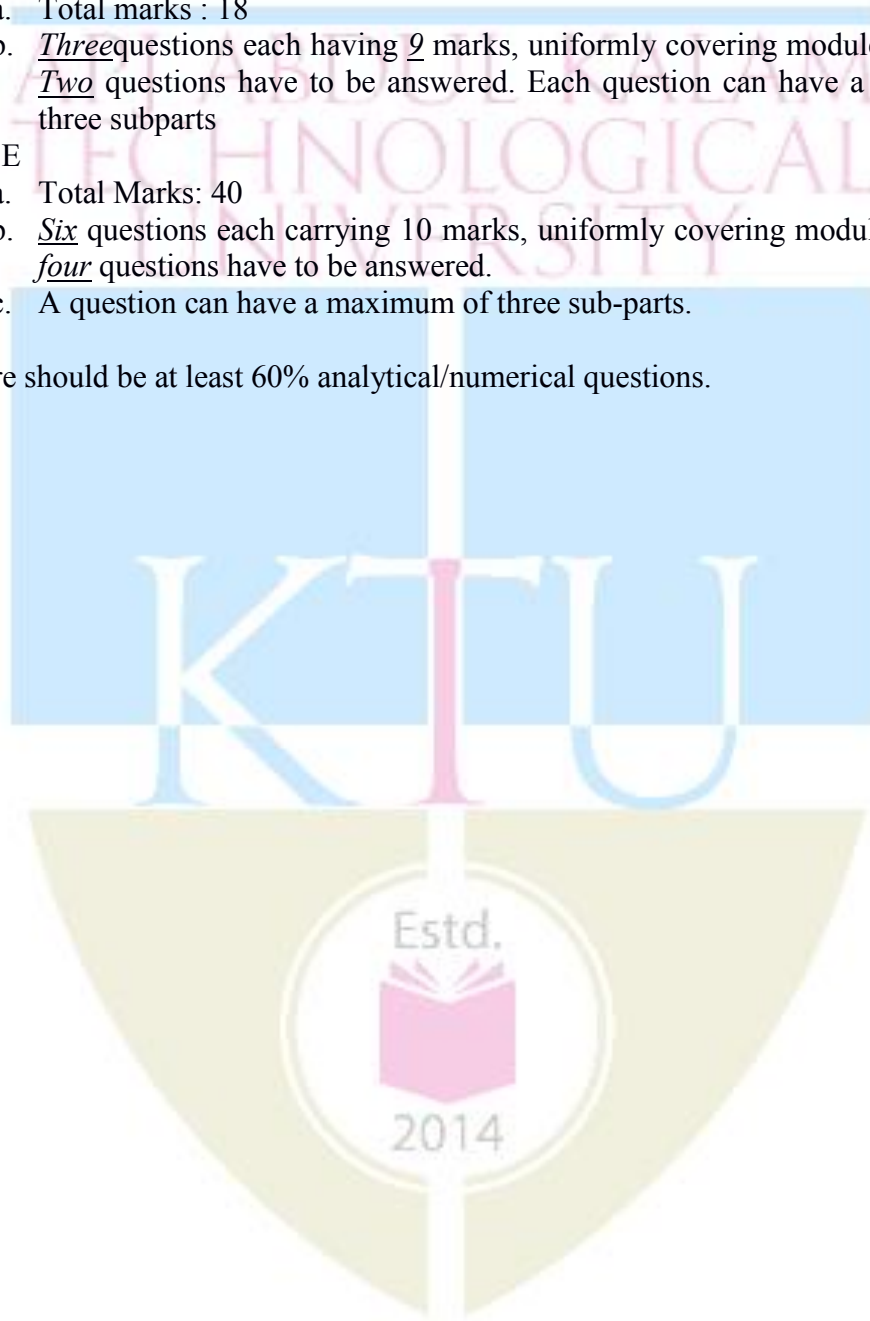
6. Part E

a. Total Marks: 40

b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.

c. A question can have a maximum of three sub-parts.

7. There should be at least 60% analytical/numerical questions.



Course code	Course Name	L-T-P - Credits	Year of Introduction
CS364	Mobile Computing	3-0-0-3	2016
<b>Pre-requisite:</b> CS307 Data Communication			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>To impart basic understanding of the wireless communication systems.</li> <li>To expose students to various aspects of mobile and ad-hoc networks.</li> </ul>			
<b>Syllabus</b>			
Mobile Computing Application and Services, Mobile Computing Architecture, Emerging Technologies, Intelligent Networks and Internet, Wireless LAN, MAC layer routing, Mobile transport layer Security Issues in mobile computing.			
<b>Expected Outcome</b>			
Student is able to			
<ol style="list-style-type: none"> <li>1. Explain various Mobile Computing application, services and architecture.</li> <li>2. Understand various technology trends for next generation cellular wireless networks.</li> <li>3. Describe protocol architecture of WLAN technology.</li> <li>4. Understand Security Issues in mobile computing.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>1. Asoke K. Talukder, Hasan Ahmad, Mobile Computing Technology- Application and Service Creation, 2<sup>nd</sup> Edition, McGraw Hill Education.</li> <li>2. Jochen Schiller, Mobile Communications, Pearson Education Asia, 2008.</li> <li>3. Jonathan Rodriguez , Fundamentals of 5G Mobile Networks, ,Wiley Publishers, 2015</li> <li>4. Theodore S. Rappaport, Wireless Communications Principles and Practice, 2/e, PHI, New Delhi, 2004.</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>1. Andrew S. Tanenbaum, Computer Networks, PHI, Third edition, 2003.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
I	Introduction to mobile computing, Middleware and Gateways, Application and services, Internet-Ubiquitous networks, Architecture and three-tier architecture for Mobile Computing, Design consideration for Mobile Computing.	06	15%
II	Spread spectrum – Direct sequence, Frequency hopping. Medium Access Control - SDMA, FDMA, TDMA, CDMA, Cellular concepts- channel assignment strategy- hand off strategy interface and system capacity- improving coverage and capacity in cellular system, Satellite Systems-GEO, LEO, MEO. Wireless Communication Systems- Telecommunication Systems- GSM-GSM services & features, architecture -DECT features & characteristics, architecture.	06	15%
<b>FIRST INTERNAL EXAM</b>			
III	Wireless LANS: Wireless LAN Standards – IEEE 802 Protocol Architecture, IEEE 802.11 System Architecture, Protocol Architecture & Services, Cellular Networks: Channel allocation, multiple access, location management, Handoffs. MAC Layer & Management, Routing - Classification of Routing	07	15%

	Algorithms, Algorithms such as DSR, AODV, DSDV, Mobile Agents, Service Discovery.		
<b>IV</b>	Mobile internet-mobile network layer-mobile IP-dynamic host configuration protocol-, mobile transport layer-implications of TCP on mobility-indirect TCP-snooping TCP- mobile TCP transmission-selective retransmission, Transaction oriented TCP- Support for mobility-file systems-WAP.	<b>07</b>	<b>15%</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	Mobile Transport Layer - Conventional TCP/IP Protocols, Indirect TCP, Snooping TCP, Mobile TCP, Other Transport Layer Protocols for Mobile Networks. Protocols and Platforms for Mobile Computing - WAP, Bluetooth, XML, J2ME, JavaCard, PalmOS, Linux for Mobile Devices, Android.	<b>08</b>	<b>20%</b>
<b>VI</b>	Security issues in mobile computing, Information Security, Components of Information Security, Next Generation Networks-LTE – Architecture & Interface – LTE radio planning and tools, 5G architecture, MIMO, Super core concept, Features and Application Case Study – Setting up an adhoc network system, LiFi.	<b>08</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

### Question Paper Pattern

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  - a. Total marks : 12
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3. Part B
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three subparts.
4. Part C
  - a. Total marks : 12
  - b. Four questions each having 3 marks, uniformly covering modules III and IV; Allfour questions have to be answered.
5. Part D
  - a. Total marks : 18
  - b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
6. Part E
  - a. Total Marks: 40
  - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
  - c. A question can have a maximum of three sub-parts.

Course code	Course Name	L-T-P Credits	Year of Introduction
CS366	Natural language processing	3-0-0-3	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>• To introduce the fundamentals of Language processing from the algorithmic viewpoint.</li> <li>• To discuss various issues those make natural language processing a hard task.</li> <li>• To discuss some applications of Natural Language Processing (NLP).</li> </ul>			
<b>Syllabus</b>			
Levels of Language Analysis, Syntax, Semantics and Pragmatics of Natural Language, Language Processing, Issues and approaches to solutions, Applications of Natural Language Processing (NLP).			
<b>Expected Outcome</b>			
The student able to			
<ol style="list-style-type: none"> <li>1. appreciate the fundamental concepts of Natural Language Processing.</li> <li>2. design algorithms for NLP tasks.</li> <li>3. develop useful systems for language processing and related tasks involving text processing.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>1. D. Jurafsky and J. H. Martin, Speech and Language Processing, Prentice Hall India, 2000</li> <li>2. James Allen, Natural Language Understanding, 2e, The Benjamin/Cummings Publishing Company Inc., Redwood City, CA.</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>1. Charniak, Eugene, Introduction to Artificial intelligence, Addison-Wesley, 1985..</li> <li>2. Ricardo Baeza-Yates and Berthier Ribeiro-Neto, Modern Information Retrieval, Addison-Wesley, 1999.</li> <li>3. U. S. Tiwary and Tanveer Siddiqui, Natural Language Processing and Information Retrieval, Oxford University Press, 2008.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
I	Introduction to Natural Language Understanding- Levels of language analysis- Syntax, Semantics, Pragmatics. Linguistic Background- An Outline of English Syntax.	8	15%
II	Lexicons, POS Tagging, Word Senses. Grammars and Parsing- Features, Agreement and Augmented Grammars.	7	15%
<b>FIRST INTERNAL EXAM</b>			
III	Grammars for Natural Language, Parsing methods and Efficient Parsing. Ambiguity Resolution- Statistical Methods. Probabilistic Context Free Grammar.	9	15%
IV	Semantics and Logical Form: Linking Syntax and Semantics- Ambiguity Resolution- other Strategies for Semantic Interpretation- Scoping and the Interpretation of Noun Phrases.	6	15%
<b>SECOND INTERNAL EXAM</b>			
V	Knowledge Representation and Reasoning- Local Discourse	8	20%



	Context and Reference- Using World Knowledge- Discourse Structure- Defining a Conversational Agent.		
<b>VI</b>	Applications- Machine Translation, Information Retrieval and Extraction, Text Categorization and Summarization.	<b>4</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

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Estd.



2014

Course code	Course Name	L-T-P - Credits	Year of Introduction
CS368	Web Technologies	3-0-0-3	2016
<b>Prerequisite: Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>• To impart the design, development and implementation of Dynamic Web Pages.</li> <li>• To develop programs for Web using Scripting Languages.</li> <li>• To give an introduction to Data Interchange formats in Web.</li> </ul>			
<b>Syllabus</b>			
Basics of Internet and World Wide Web, HTML and XHTML, Cascading Style Sheets, Frameworks, Basics of JavaScript, JQuery, Introduction to XML and JSON, Overview of PHP			
<b>Expected Outcome</b>			
The student will be able to			
<ol style="list-style-type: none"> <li>i. Understand different components in web technology and to know about CGI and CMS.</li> <li>ii. Develop interactive Web pages using HTML/XHTML.</li> <li>iii. Present a professional document using Cascaded Style Sheets.</li> <li>iv. Construct websites for user interactions using JavaScript and JQuery.</li> <li>v. Know the different information interchange formats like XML and JSON.</li> <li>vi. Develop Web applications using PHP.</li> </ol>			
<b>Text Books</b>			
<ol style="list-style-type: none"> <li>1. P. J. Deitel, H.M. Deitel, Internet &amp; World Wide Web How To Program, 4/e, Pearson International Edition 2010.</li> <li>2. Robert W Sebesta, Programming the World Wide Web, 7/e, Pearson Education Inc., 2014.</li> </ol>			
<b>References</b>			
<ol style="list-style-type: none"> <li>1. Bear Bibeault and Yehuda Katz, jQuery in Action, Second Edition, Manning Publications.[Chapter 1] Black Book, Kogent Learning Solutions Inc. 2009.</li> <li>2. Bob Boiko, Content Management Bible, 2<sup>nd</sup> Edition, Wiley Publishers. [Chapter 1, 2]</li> <li>3. Chris Bates, Web Programming Building Internet Applications, 3/e, Wiley India Edition 2009.</li> <li>4. Dream Tech, Web Technologies: HTML, JS, PHP, Java, JSP, ASP.NET, XML, AJAX,</li> <li>5. Jeffrey C Jackson, Web Technologies A Computer Science Perspective, Pearson Education Inc. 2009.</li> <li>6. Lindsay Bassett, Introduction to JavaScript Object Notation: A To-the-Point Guide to JSON 1st Edition, O'Reilly.[Chapter 1,2,3,4]</li> <li>7. Matthew MacDonald, WordPress: The Missing Manual, 2nd Edition, O'Reilly Media. [Chapter 1]</li> </ol>			
<b>Web Resources</b>			
<ol style="list-style-type: none"> <li>1. <a href="http://www.w3.org/CGI/">www.w3.org/CGI/</a></li> <li>2. <a href="http://old.tree.ro/en/strategy-white-papers/content-management-systems.pdf">old.tree.ro/en/strategy-white-papers/content-management-systems.pdf</a></li> <li>3. <a href="http://httpd.apache.org/download.cgi">httpd.apache.org/download.cgi</a></li> <li>4. <a href="https://alistapart.com/article/frameworks">https://alistapart.com/article/frameworks</a></li> <li>5. <a href="http://getbootstrap.com/css/">http://getbootstrap.com/css/</a></li> <li>6. <a href="https://www.w3.org/TR/WD-DOM/introduction.html">https://www.w3.org/TR/WD-DOM/introduction.html</a></li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks

I	<b>Introduction to the Internet:</b> The World Wide Web, Web Browsers, Web Servers, Uniform Resource Locators, Multipurpose Internet Mail Extensions, The Hypertext Transfer Protocol. Common Gateway Interface(CGI), Content Management System – Basics <i>Case Study:</i> Apache Server, WordPress.	06	15%
II	<b>Introduction to HTML/XHTML :</b> Origins and Evolution of HTML and XHTML, Basic Syntax of HTML, Standard HTML Document Structure, Basic Text Markup, Images, Hypertext Links, Lists, Tables, Forms, HTML5, Syntactic Differences between HTML and XHTML.	07	15%
FIRST INTERNAL EXAM			
III	<b>Introduction to Styles sheets and Frameworks</b> <b>Cascading Style Sheets:</b> Levels of Style Sheets - Style Specification Formats, Selector Forms, Property-Value Forms, Font Properties, List Properties, Alignment of Text, Color, The Box Model, Background Images, The span and div Tags. <b>Frameworks:</b> Overview and Basics of Responsive CSS Frameworks - Bootstrap.	06	15%
IV	<b>Introduction to JavaScript and jQuery</b> <b>The Basics of JavaScript:</b> Overview of JavaScript, Object Orientation and JavaScript, General Syntactic Characteristics-Primitives, Operations, and Expressions, Screen Output and Keyboard Input, Control Statements, Object Creation and Modification, Arrays, Functions. Callback Functions, JavaScript HTML DOM. <b>Introduction to jQuery:</b> Overview and Basics.	07	15%
SECOND INTERNAL EXAMINATION			
V	<b>Introduction to Data Interchange Formats</b> <b>XML:</b> The Syntax of XML, XML Document Structure, Namespaces, XML Schemas, Displaying Raw XML Documents, Displaying XML Documents with CSS, XSLT Style Sheets, XML Applications. <b>JSON(Basics Only):</b> Overview, Syntax, Datatypes, Objects, Schema, Comparison with XML.	08	20%
VI	<b>Introduction to PHP:</b> Origins and Uses of PHP, Overview of PHP - General Syntactic Characteristics - Primitives, Operations, and Expressions - Control Statements, Arrays, Functions, Pattern Matching, Form Handling, Cookies, Session Tracking.	08	20%
END SEMESTER EXAM			

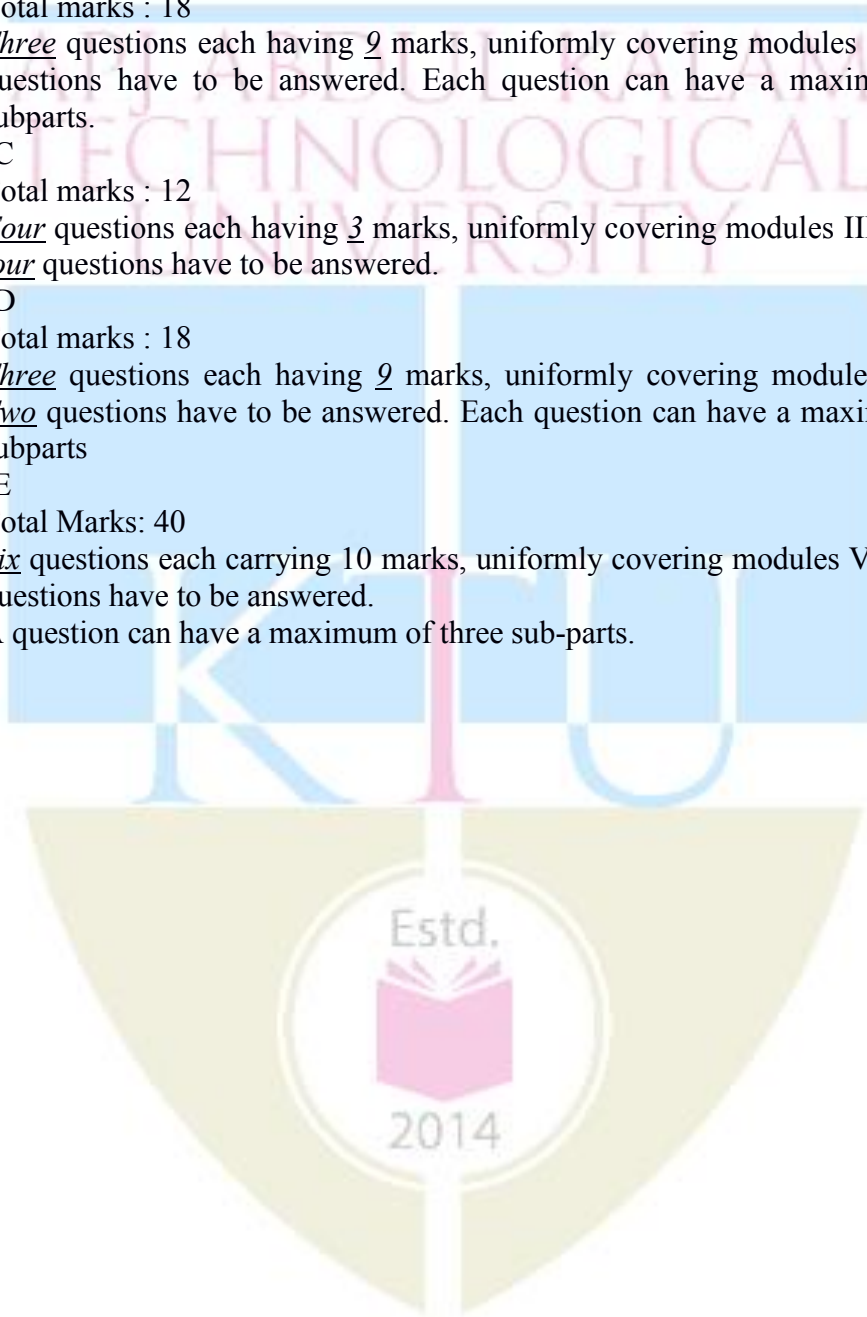
**Assignment:**

It is highly recommended to give assignment based on:

1. JavaScript Frameworks (like AngularJS or/and NodeJS)
2. Any PHP web app based on frameworks (like Laravel, CodeIgniter, CakePHP, Zend etc.)

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  - a. Total Marks: 40
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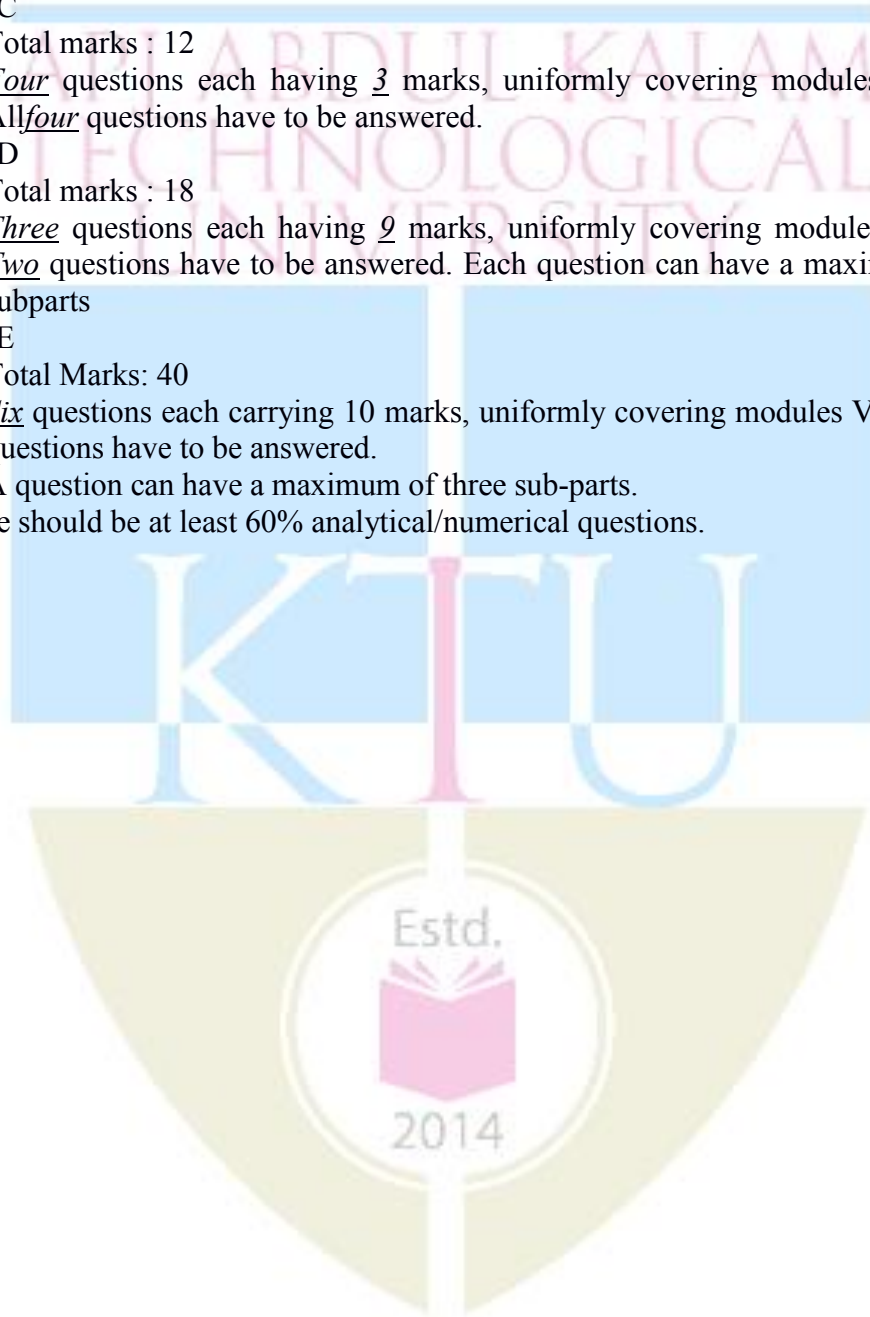
Course code.	Course Name	L-T-P - Credits	Year of Introduction
CS372	HIGH PERFORMANCE COMPUTING	3-0-0-3	2016
<b>Pre-requisites : CS202 Computer Organization and Architecture</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>• To introduce the concepts of Modern Processors.</li> <li>• To introduce Optimization techniques for serial code.</li> <li>• To introduce Parallel Computing Paradigms.</li> <li>• To introduce Parallel Programming using OpenMP and MPI.</li> </ul>			
<b>Syllabus</b>			
Modern processors - pipelining-superscalarity-multicore processors- Mutithreaded processors- vector processors- basic optimization techniques for serial code - taxonomy of parallel computing paradigms- shared memory computers- distributed-memory computers- Hierarchical Systems- networks- basics of parallelization - data parallelism - function parallelism- Parallel scalability- shared memory parallel programming with OpenMp - Distributed-memory parallel programming with MPI.			
<b>Expected Outcome</b>			
The students will be able to			
<ol style="list-style-type: none"> <li>i. appreciate the concepts used in Modern Processors for increasing the performance.</li> <li>ii. appreciate Optimization techniques for serial code.</li> <li>iii. appreciate Parallel Computing Paradigms.</li> <li>iv. identify the performance issues in Parallel Programming using OpenMP and MPI.</li> </ol>			
<b>Text Book</b>			
1. Georg Hager, Gerhard Wellein, Introduction to High Performance Computing for Scientists and Engineers, Chapman & Hall / CRC Computational Science series, 2011.			
<b>References</b>			
<ol style="list-style-type: none"> <li>1. Charles Severance, Kevin Dowd, High Performance Computing, O'Reilly Media, 2nd Edition, 1998.</li> <li>2. Kai Hwang, Faye Alaye Briggs, Computer Architecture and Parallel Processing, McGraw Hill, 1984.</li> </ol>			
<b>Course Plan</b>			
Module	Contents	Hours	End Sem. Exam Marks
I	Modern Processors : Stored Program Computer Architecture- General purpose cache- based microprocessor-Performance based metrics and benchmarks- Moore's Law- Pipelining- Superscalarity- SIMD- Memory Hierarchies Cache- mapping- prefetch- Multicore processors- Mutithreaded processors- Vector Processors- Design Principles- Maximum performance estimates- Programming for vector architecture.	07	15%

<b>II</b>	Basic optimization techniques for serial code : scalar profiling- function and line based runtime profiling- hardware performance counters- common sense optimizations- simple measures, large impact- elimination of common subexpressions- avoiding branches- using simd instruction sets- the role of compilers - general optimization options- inlining - aliasing- computational accuracy- register optimizations- using compiler logs- c++ optimizations - temporaries- dynamic memory management- loop kernels and iterators data access optimization: balance analysis and light speed estimates- storage order- case study: jacobi algorithm and dense matrix transpose.	<b>07</b>	<b>15%</b>
<b>FIRST INTERNAL EXAM</b>			
<b>III</b>	Parallel Computers : Taxonomy of parallel computing paradigms- Shared memory computers- Cache coherence- UMA - ccNUMA- Distributed-memory computers- Hierarchical systems- Networks- Basic performance characteristics- Buses- Switched and fat- tree networks- Mesh networks- Hybrids - Basics of parallelization - Why parallelize - Data Parallelism - Function Parallelism- Parallel Scalability- Factors that limit parallel execution- Scalability metrics- Simple scalability laws- parallel efficiency - serial performance Vs Strong scalability- Refined performance models- Choosing the right scaling baseline- Case Study: Can slow processors compute faster- Load balance.	<b>07</b>	<b>15%</b>
<b>IV</b>	Distributed memory parallel programming with MPI : message passing - introduction to MPI – example - messages and point-to-point communication - collective communication – nonblocking point-to-point communication- virtual topologies - MPI parallelization of Jacobi solver- MPI implementation - performance properties	<b>08</b>	<b>15%</b>
<b>SECOND INTERNAL EXAM</b>			
<b>V</b>	Shared memory parallel programming with OpenMp : introduction to OpenMp - parallel execution - data scoping- OpenMp work sharing for loops- synchronization - reductions - loop scheduling - tasking - case study: OpenMp- parallel jacobi algorithm- advanced OpenMpwavefront parallelization- Efficient OpenMP programming: Profiling OpenMP programs - Performance pitfalls- Case study: Parallel Sparse matrix-vector multiply.	<b>08</b>	<b>20%</b>
<b>VI</b>	Efficient MPI programming : MPI performance tools- communication parameters- Synchronization, serialization, contention- Reducing communication overhead- optimal domain decomposition- Aggregating messages – Nonblocking Vs Asynchronous communication- Collective communication- Understanding intra-node point-to-point communication.	<b>08</b>	<b>20%</b>
<b>END SEMESTER EXAM</b>			

### Question Paper Pattern

1. There will be *five* parts in the question paper – A, B, C, D, E
2. Part A
  - a. Total marks : 12

- b. Four questions each having 3 marks, uniformly covering modules I and II; Allfour questions have to be answered.
  3. Part B
    - a. Total marks : 18
    - b. Three questions each having 9 marks, uniformly covering modules I and II; Two questions have to be answered. Each question can have a maximum of three subparts.
  4. Part C
    - a. Total marks : 12
    - b. Four questions each having 3 marks, uniformly covering modules III and IV; Allfour questions have to be answered.
  5. Part D
    - a. Total marks : 18
    - b. Three questions each having 9 marks, uniformly covering modules III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
  6. Part E
    - a. Total Marks: 40
    - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
    - c. A question can have a maximum of three sub-parts.
  7. There should be at least 60% analytical/numerical questions.



Course code	Course Name	L-T-P-Credits	Year of Introduction
CS332	MICROPROCESSOR LAB	0-0-3-1	2016
<b>Pre-requisite:</b> CS305 Microprocessors and Microcontrollers			
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To practice assembly language programming on 8086.</li> <li>• To practice fundamentals of interfacing/programming various peripheral devices with microprocessor/microcontroller.</li> </ul>			
<b>List of Exercises/ Experiments: (Minimum 12 Exercises/ Experiments are mandatory. Exercises/ Experiments marked with * are mandatory)</b>			
<b>I. Assembly Language Programming Exercises/Experiments using 8086 Trainer kit</b> <ol style="list-style-type: none"> <li>1. Implementation of simple decimal arithmetic and bit manipulation operations.*</li> <li>2. Implementation of code conversion between BCD, Binary, Hexadecimal and ASCII.</li> <li>3. Implementation of searching and sorting of 16-bit numbers.</li> <li>4. Programming exercises using stack and subroutines.*</li> </ol>			
<b>II. Exercises/Experiments using MASM (PC Required)</b> <ol style="list-style-type: none"> <li>5. Study of Assembler and Debugging commands.</li> <li>6. Implementation of decimal arithmetic( 16 and 32 bit) operations.*</li> <li>7. Implementation of String manipulations.*</li> <li>8. Implementation of searching and sorting of 16-bit numbers.</li> <li>9. Implementation of Matrix operations like addition, transpose, multiplication etc.</li> </ol>			
<b>III. Interfacing Exercises/Experiments with 8086 trainer kit through Assembly Language Programming</b> <ol style="list-style-type: none"> <li>10. Interfacing with stepper motor - Rotate through any given sequence.*</li> <li>11. Interfacing with 8255 (mode0 and mode1 only).*</li> <li>12. Interfacing with 8279 (Rolling message, 2 key lock out and N-key roll over implementation).*</li> <li>13. Interfacing with 8253/54 Timer/Counter.</li> <li>14. Interfacing with Digital-to-Analog Converter.*</li> <li>15. Interfacing with Analog-to- Digital Converter.</li> <li>16. Interfacing with 8259 Interrupt Controller.</li> </ol>			
<b>IV. Exercises/Experiments using 8051 trainer kit</b> <ol style="list-style-type: none"> <li>17. Familiarization of 8051 trainer kit by executing simple Assembly Language programs such as decimal arithmetic and bit manipulation.*</li> <li>18. Implementation of Timer programming (in mode1).</li> <li>19. Implementation of stepper motor interfacing, ADC/DAC interfacing and sensor interfacing with 8251 through Assembly Language programming.</li> </ol>			
<b>Expected Outcome</b> The students will be able to <ol style="list-style-type: none"> <li><i>i.</i> Develop assembly language programs for problem solving using software interrupts and various assembler directives.</li> <li><i>ii.</i> Implement interfacing of various I/O devices to the microprocessor/microcontroller through assembly language programming.</li> </ol>			



Course code	Course Name	L-T-P-Credits	Year of Introduction
CS334	Network Programming Lab	0-0-3-1	2016
<b>Pre-requisite:</b> CS307 Data Communication			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>• To introduce Network related commands and configuration files in Linux Operating System.</li> <li>• To introduce tools for Network Traffic Analysis and Network Monitoring.</li> <li>• To practice Network Programming using Linux System Calls.</li> <li>• To design and deploy Computer Networks.</li> </ul>			
<b>List of Exercises/ Experiments (12 Exercises/ Experiments are to be completed . Exercises/ Experiments marked with * are mandatory)</b>			
<ol style="list-style-type: none"> <li>1. Getting started with Basics of Network configurations files and Networking Commands in Linux.</li> <li>2. To familiarize and understand the use and functioning of System Calls used for Operating system and network programming in Linux.</li> <li>3. <u>Familiarization and implementation of programs related to Process and thread.</u></li> <li>4. <u>Implement the First Readers-Writers Problem.</u></li> <li>5. <u>Implement the Second Readers-Writers problem.</u></li> <li>6. <u>Implement programs for Inter Process Communication using PIPE, Message Queue and Shared Memory.</u></li> <li>7. Implement Client-Server communication using Socket Programming and TCP as transport layer protocol.*</li> <li>8. Implement Client-Server communication using Socket Programming and UDP as transport layer protocol.*</li> <li>9. Implement a multi user chat server using TCP as transport layer protocol.*</li> <li>10. Implement Concurrent Time Server application using UDP to execute the program at remoteserver. Client sends a time request to the server, server sends its system time back to the client. Client displays the result.*</li> <li>11. Implement and simulate algorithm for Distance vector routing protocol.</li> <li>12. Implement and simulate algorithm for Link state routing protocol.</li> <li>13. Implement Simple Mail Transfer Protocol.*</li> <li>14. Develop concurrent file server which will provide the file requested by client if it exists. If not server sends appropriate message to the client. Server should also send its process ID (PID) to clients for display along with file or the message.*</li> <li>15. Using Wireshark observe data transferred in client server communication using UDP and identify the UDP datagram.</li> <li>16. Using Wireshark observe Three Way Handshaking Connection Establishment, Data Transfer and Three Way Handshaking Connection Termination in client server communication using TCP.</li> <li>17. Develop a packet capturing and filtering application using raw sockets.</li> <li>18. Design and configure a network with multiple subnets with wired and wireless LANs using required network devices. Configure the following services in the network- TELNET, SSH, FTP server, Web server, File server, DHCP server and DNS server.*</li> <li>19. Install network simulator NS-2 in any of the Linux operating system and simulate wired and wireless scenarios.</li> </ol>			
<b>Expected Outcome</b>			
The students will be able to			
<ol style="list-style-type: none"> <li>1. Use network related commands and configuration files in Linux Operating System.</li> <li>2. Develop operating system and network application programs.</li> <li>3. Analyze network traffic using network monitoring tools.</li> </ol>			

Course code	Course Name	L-T-P - Credits	Year of Introduction						
**341	DESIGN PROJECT	0-1-2-2	2016						
<b>Prerequisite : Nil</b>									
<p><b>Course Objectives</b></p> <ul style="list-style-type: none"> <li>• To understand the engineering aspects of design with reference to simple products</li> <li>• To foster innovation in design of products, processes or systems</li> <li>• To develop design that add value to products and solve technical problems</li> </ul>									
<p><b>Course Plan</b></p> <p><b>Study :</b>Take minimum three simple products, processes or techniques in the area of specialisation, study, analyse and present them. The analysis shall be focused on functionality, strength, material, manufacture/construction, quality, reliability, aesthetics, ergonomics, safety, maintenance, handling, sustainability, cost etc. whichever are applicable. Each student in the group has to present individually; choosing different products, processes or techniques.</p> <p><b>Design:</b> The project team shall identify an innovative product, process or technology and proceed with detailed design. At the end, the team has to document it properly and present and defend it. The design is expected to concentrate on functionality, design for strength is not expected.</p> <p><i>Note :</i> The one hour/week allotted for tutorial shall be used for discussions and presentations. The project team (not exceeding four) can be students from different branches, if the design problem is multidisciplinary.</p>									
<p><b>Expected outcome.</b></p> <p>The students will be able to</p> <ol style="list-style-type: none"> <li>i. Think innovatively on the development of components, products, processes or technologies in the engineering field</li> <li>ii. Analyse the problem requirements and arrive workable design solutions</li> </ol>									
<p><b>Reference:</b></p> <p>Michael Luchs, Scott Swan, Abbie Griffin, 2015. Design Thinking. 405 pages, John Wiley &amp; Sons, Inc</p>									
<p><b>Evaluation</b></p> <table style="width: 100%; border: none;"> <tr> <td style="width: 70%;">First evaluation ( Immediately after first internal examination )</td> <td style="text-align: right;">20 marks</td> </tr> <tr> <td>Second evaluation ( Immediately after second internal examination)</td> <td style="text-align: right;">20 marks</td> </tr> <tr> <td>Final evaluation ( Last week of the semester)</td> <td style="text-align: right;">60 marks</td> </tr> </table> <p><i>Note:</i> All the three evaluations are mandatory for course completion and for awarding the final grade.</p>				First evaluation ( Immediately after first internal examination )	20 marks	Second evaluation ( Immediately after second internal examination)	20 marks	Final evaluation ( Last week of the semester)	60 marks
First evaluation ( Immediately after first internal examination )	20 marks								
Second evaluation ( Immediately after second internal examination)	20 marks								
Final evaluation ( Last week of the semester)	60 marks								

Course code	Course Name	L-T-P - Credits	Year of Introduction
**352	Comprehensive Examination	0-1-1-2	2016
<b>Prerequisite : Nil</b>			
<b>Course Objectives</b>			
<ul style="list-style-type: none"> <li>To assess the comprehensive knowledge gained in basic courses relevant to the branch of study</li> <li>To comprehend the questions asked and answer them with confidence.</li> </ul>			
<b>Assessment</b>			
<p><b>Oral examination</b> – To be conducted by the college (@ three students/hour) covering all the courses up to and including V semester– 50 marks</p> <p><b>Written examination</b> - To be conducted by the Dept. on the date announced by the University– common to all students of the same branch – objective type ( 1 hour duration)– 50 multiple choice questions ( 4 choices) of 1 mark each covering the six common courses of S1&amp;S2 and six branch specific courses listed – questions are set by the University - no negative marks – 50 marks.</p> <p><i>Note:</i> Both oral and written examinations are mandatory. But separate minimum marks is not insisted for pass. If a students does not complete any of the two assessments, grade I shall be awarded and the final grade shall be given only after the completion of both the assessments. The two hours allotted for the course may be used by the students for discussion, practice and for oral assessment.</p>			
<b>Expected outcome.</b>			
<ul style="list-style-type: none"> <li>The students will be confident in discussing the fundamental aspects of any engineering problem/situation and give answers in dealing with them</li> </ul>			



Course code	Course Name	L-T-P - Credits	Year of Introduction
<b>**451</b>	<b>Seminar and Project Preliminary</b>	<b>0-1-4-2</b>	<b>2016</b>
<b>Prerequisite : Nil</b>			
<b>Course Objectives</b> <ul style="list-style-type: none"> <li>• To develop skills in doing literature survey, technical presentation and report preparation.</li> <li>• To enable project identification and execution of preliminary works on final semester project</li> </ul>			
<b>Course Plan</b> <b>Seminar:</b> Each student shall identify a topic of current relevance in his/her branch of engineering, get approval of faculty concerned, collect sufficient literature on the topic, study it thoroughly, prepare own report and present in the class. <b>Project preliminary:</b> Identify suitable project relevant to the branch of study. Form project team ( not exceeding four students). The students can do the project individually also. Identify a project supervisor. Present the project proposal before the assessment board (excluding the external expert) and get it approved by the board. The preliminary work to be completed: (1) Literature survey (2) Formulation of objectives (3) Formulation of hypothesis/design/methodology (4) Formulation of work plan (5) Seeking funds (6) Preparation of preliminary report <b>Note:</b> The same project should be continued in the eighth semester by the same project team.			
<b>Expected outcome.</b> The students will be able to <ol style="list-style-type: none"> <li>i. Analyse a current topic of professional interest and present it before an audience</li> <li>ii. Identify an engineering problem, analyse it and propose a work plan to solve it.</li> </ol>			
<b>Evaluation</b> Seminar : <b>50 marks</b> (Distribution of marks for the seminar is as follows: i. Presentation : 40% ii. Ability to answer questions : 30% & iii. Report : 30%) Project preliminary : <b>50 marks</b> ( Progress evaluation by the supervisor : 40% and progress evaluation by the assessment board excluding external expert : 60%. Two progress evaluations, mid semester and end semester, are mandatory.)  <i>Note:</i> All evaluations are mandatory for course completion and for awarding the final grade.			

Course code	Course Name	Credits	Year of Introduction						
**492	PROJECT	6	2016						
<b>Prerequisite : Nil</b>									
<b>Course Objectives</b>									
<ul style="list-style-type: none"> <li>• To apply engineering knowledge in practical problem solving</li> <li>• To foster innovation in design of products, processes or systems</li> <li>• To develop creative thinking in finding viable solutions to engineering problems</li> </ul>									
<b>Course Plan</b>									
<p>In depth study of the topic assigned in the light of the preliminary report prepared in the seventh semester</p> <p>Review and finalization of the approach to the problem relating to the assigned topic</p> <p>Preparing a detailed action plan for conducting the investigation, including team work</p> <p>Detailed Analysis/Modelling/Simulation/Design/Problem Solving/Experiment as needed</p> <p>Final development of product/process, testing, results, conclusions and future directions</p> <p>Preparing a paper for Conference presentation/Publication in Journals, if possible</p> <p>Preparing a report in the standard format for being evaluated by the dept. assessment board</p> <p>Final project presentation and viva voce by the assessment board including external expert</p>									
<b>Expected outcome</b>									
<p>The students will be able to</p> <ul style="list-style-type: none"> <li>iii. Think innovatively on the development of components, products, processes or technologies in the engineering field</li> <li>iv. Apply knowledge gained in solving real life engineering problems</li> </ul>									
<b>Evaluation</b>									
<b>Maximum Marks : 100</b>									
<table style="width: 100%; border: none;"> <tr> <td style="width: 50%;">(i) Two progress assessments</td> <td style="width: 50%;">20% by the faculty supervisor(s)</td> </tr> <tr> <td>(ii) Final project report</td> <td>30% by the assessment board</td> </tr> <tr> <td>(iii) Project presentation and viva voce</td> <td>50% by the assessment board</td> </tr> </table>				(i) Two progress assessments	20% by the faculty supervisor(s)	(ii) Final project report	30% by the assessment board	(iii) Project presentation and viva voce	50% by the assessment board
(i) Two progress assessments	20% by the faculty supervisor(s)								
(ii) Final project report	30% by the assessment board								
(iii) Project presentation and viva voce	50% by the assessment board								
<p><i>Note:</i> All the three evaluations are mandatory for course completion and for awarding the final grade.</p>									

Estd.

2014